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This paper must be cited as:

Gutiérrez Gil, R.; Lucas Alba, S. (2020). Automatically Proving and Disproving Feasibility Conditions. Springer Nature. 416-435. https://doi.org/10.1007/978-3-030-51054-1_27



The final publication is available at

https://doi.org/10.1007/978-3-030-51054-1_27

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Additional Information

Automatically Proving and Disproving Feasibility Conditions^{*}

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Abstract. In the realm of term rewriting, given terms s and t , a reachability condition $s \rightarrow^* t$ is called *feasible* if there is a substitution σ such that $\sigma(s)$ rewrites into $\sigma(t)$ in zero or more steps; otherwise, it is called *infeasible*. Checking infeasibility of (sequences of) reachability conditions is important in the analysis of computational properties of rewrite systems like confluence or (operational) termination. In this paper, we generalize this notion of feasibility to arbitrary n -ary relations on terms defined by first-order theories. In this way, properties of computational systems whose operational semantics can be given as a first-order theory can be investigated. We introduce a framework for proving feasibility/infeasibility, and a new tool, `infChecker`, which implements it.

Keywords: Conditional rewriting · Feasibility · Program analysis.

1 Introduction

The *(in)feasibility* of sequences of goals $s \rightarrow^* t$ representing many step rewritings in Conditional Term Rewriting Systems (CTRSs, see [22, Section 7]) has been investigated by several authors. The word “feasibility” refers to the possibility of applying a substitution σ as part of the desired test, i.e., checking whether $\sigma(s) \rightarrow_{\mathcal{R}}^* \sigma(t)$ holds for some substitution σ , rather than just checking $s \rightarrow_{\mathcal{R}}^* t$ (reachability test). The use of (in)feasibility tests in confluence and (operational) termination analysis of CTRSs has been investigated elsewhere (see, e.g., [13,25] and the references therein). We generalize “feasibility of a reachability problem” by defining *feasibility conditions, sequences and goals* without any specific reference to rewriting systems or rewriting goals. Instead, we rely on first-order logic and use (two layered) sequences of *atoms* headed with a predicate \bowtie as feasibility goals. The meaning of predicates \bowtie is given by using first-order theories Th_{\bowtie} by provability of the corresponding atoms. New properties (also of CTRSs) can be investigated in this way.

^{*} Supported by EU (FEDER), and projects RTI2018-094403-B-C32, PROMETEO/2019/098, and SP20180225. Also by INCIBE program “Ayudas para la excelencia de los equipos de investigación avanzada en ciberseguridad” (Raúl Gutiérrez).

$$le(0, s(y)) \rightarrow true \quad (1)$$

$$le(s(x), s(y)) \rightarrow le(x, y) \quad (2)$$

$$le(x, 0) \rightarrow false \quad (3)$$

$$min(cons(x, nil)) \rightarrow x \quad (4)$$

$$min(cons(x, xs)) \rightarrow x \Leftarrow min(xs) \approx y, le(x, y) \approx true \quad (5)$$

$$min(cons(x, xs)) \rightarrow y \Leftarrow min(xs) \approx y, le(x, y) \approx false \quad (6)$$

Fig. 1. CTRS 551.trs in COPS database of confluence problems.

by *specializing* $(C)_{f,i}$ for each k -ary symbol f in the signature \mathcal{F} and $1 \leq i \leq k$ and $(R1)_\alpha$ for all conditional rules $\alpha : \ell \rightarrow r \Leftarrow c$ in \mathcal{R} . Rules in $\mathcal{I}(\mathcal{R})$ are *schematic*: each inference rule $\frac{B_1 \dots B_n}{A}$ can be used under any *instance* $\frac{\sigma(B_1) \dots \sigma(B_n)}{\sigma(A)}$ of the rule by a substitution σ . We write $s \rightarrow_{\mathcal{R}} t$ (resp. $s \rightarrow_{\mathcal{R}}^* t$) iff there is a proof tree for $s \rightarrow t$ (resp. $s \rightarrow^* t$) using $\mathcal{I}(\mathcal{R})$. Operational termination of \mathcal{R} is defined as the absence of infinite proof trees for goals $s \rightarrow t$ and $s \rightarrow^* t$ in $\mathcal{I}(\mathcal{R})$ [14].

A *structure* \mathcal{A} for a first-order language is an interpretation of the function and predicate symbols (f, g, \dots and P, Q, \dots , respectively) as mappings $f^{\mathcal{A}}, g^{\mathcal{A}}, \dots$ and relations $P^{\mathcal{A}}, Q^{\mathcal{A}}, \dots$ on a given set (carrier) also denoted \mathcal{A} . Then, the usual interpretation of first-order formulas with respect to \mathcal{A} is considered. A model for a theory Th , i.e., a set of first-order sentences (formulas whose variables are all *quantified*), is just a structure \mathcal{A} that makes them all true, written $\mathcal{A} \models \text{Th}$. In the following, $\text{Th} \vdash \varphi$ means that formula φ is a *logical consequence* of Th . We assume the use of a sound and complete proof method, in particular Gentzen's natural deduction, see [23]. In this setting, we often assume the use of the inference rules of natural deduction [23, p. 20] to deal with logical connectives and quantifiers when necessary.

3 Feasibility of Sequences and Goals

Consider a signature Σ of function symbols and a set Π of predicate symbols. As in [4], (Σ, Π) is often called a *signature with predicates*. Let $\mathcal{F} \subseteq \Sigma$ be a signature and $\mathbb{P} \subseteq \Pi$ be a set of predicates (e.g., $\mathbb{P} = \{\rightarrow, \rightarrow^*, \downarrow, \leftrightarrow, \leftrightarrow^*, \succeq, \dots\}$).¹ Let $\mathbb{T} = \{\text{Th}_{\bowtie} \mid \bowtie \in \mathbb{P}\}$ be a \mathbb{P} -indexed set of first-order theories Th_{\bowtie} defining the predicates \bowtie in \mathbb{P} , possibly involving predicate symbols which are *not* in \mathbb{P} .

Example 2. For the CTRS \mathcal{R} in Figure 1, we obtain a theory $\overline{\mathcal{R}}$ from $\mathcal{I}(\mathcal{R})$ as follows [11, Section 4.5]: the inference rules $(\rho) \frac{B_1 \dots B_n}{A}$ in $\mathcal{I}(\mathcal{R})$ are considered as *sentences* $\overline{\rho}$ of the form $(\forall \mathbf{x}) B_1 \wedge \dots \wedge B_n \Rightarrow A$, where \mathbf{x} is the sequence of variables occurring in atoms B_1, \dots, B_n and A ; if empty, we just write $B_1 \wedge \dots \wedge B_n \Rightarrow A$ (see Figure 2). For $\mathbb{P} = \{\rightarrow, \rightarrow^*\}$, we let $\text{Th}_{\rightarrow} = \text{Th}_{\rightarrow^*} = \overline{\mathcal{R}}$.

Examples 6 and 7 illustrate and motivate the use of theories involving predicates not in \mathbb{P} .

¹ For simplicity, in our exposition we restrict the attention to *binary* predicates, but the techniques and results in this paper easily generalize to n -ary predicates.

$$\begin{aligned}
& (\forall x) x \rightarrow^* x \\
& (\forall x, y, z) x \rightarrow y \wedge y \rightarrow^* z \Rightarrow x \rightarrow^* z \\
& (\forall x, y) x \rightarrow y \Rightarrow s(x) \rightarrow s(y) \\
& (\forall x, y, z) x \rightarrow y \Rightarrow \text{cons}(x, z) \rightarrow \text{cons}(y, z) \\
& (\forall x, y, z) x \rightarrow y \Rightarrow \text{cons}(z, x) \rightarrow \text{cons}(z, y) \\
& (\forall x, y, z) x \rightarrow y \Rightarrow \text{le}(x, z) \rightarrow \text{le}(y, z) \\
& (\forall x, y, z) x \rightarrow y \Rightarrow \text{le}(z, x) \rightarrow \text{le}(z, y) \\
& (\forall x, y) x \rightarrow y \Rightarrow \text{min}(x) \rightarrow \text{min}(y) \\
& (\forall y) \text{le}(0, s(y)) \rightarrow \text{true} \\
& (\forall x, y) \text{le}(s(x), s(y)) \rightarrow \text{le}(x, y) \\
& (\forall x) \text{le}(x, 0) \rightarrow \text{false} \\
& (\forall x) \text{min}(\text{cons}(x, \text{nil})) \rightarrow x \\
& (\forall x, y, xs) \text{min}(xs) \rightarrow^* y \wedge \text{le}(x, y) \rightarrow^* \text{true} \Rightarrow \text{min}(\text{cons}(x, xs)) \rightarrow x \\
& (\forall x, xs) \text{min}(xs) \rightarrow^* y \wedge \text{le}(x, y) \rightarrow^* \text{false} \Rightarrow \text{min}(\text{cons}(x, xs)) \rightarrow y
\end{aligned}$$

Fig. 2. Theory $\overline{\mathcal{R}}$ for the CTRS \mathcal{R} in Example 2

An $(\mathcal{F}, \mathbb{P})$ -*f-condition* γ (or just *f-condition* if \mathcal{F} and \mathbb{P} are clear from the context) is an atom $s \bowtie t$ where $\bowtie \in \mathbb{P}$ and $s, t \in \mathcal{T}(\mathcal{F}, \mathcal{X})$. Sequences $F = (\gamma_i)_{i=1}^n = (\gamma_1, \dots, \gamma_n)$ of f-conditions are called *f-sequences*. A set $\mathcal{G} = \{F_1; \dots; F_m\}$ of f-sequences is called an *f-goal*; we use ‘;’ instead of ‘,’ which is already considered in f-sequences. We often drop ‘f-’ when no confusion arises. Empty sequences and goals are written $()$ and $\{\}$.

Remark 1 (Notation). In the following, we often use ‘ \in ’ to denote membership of components in both sequences and goals.

Definition 1 (Feasibility). A condition $s \bowtie t$ is (\mathbb{T}, σ) -feasible if $\text{Th}_{\bowtie} \vdash \sigma(s) \bowtie \sigma(t)$ holds; otherwise, it is (\mathbb{T}, σ) -infeasible. We also say that $s \bowtie t$ is \mathbb{T} -feasible (or Th_{\bowtie} -feasible, or just feasible if no confusion arises) if it is (\mathbb{T}, σ) -feasible for some substitution σ ; otherwise, we call it infeasible.

A sequence F is \mathbb{T} -feasible (or just feasible) iff there is a substitution σ such that, for all $\gamma \in F$, γ is (\mathbb{T}, σ) -feasible. Note that $()$ is trivially feasible. A goal \mathcal{G} is feasible iff it contains a feasible sequence $F \in \mathcal{G}$. Now, $\{\}$ is trivially infeasible.

Example 3. (continuing Example 1) We can prove a ground term t non-looping as the \mathbb{T} -infeasibility of $\mathcal{G} = \{(t \rightarrow y, y \rightarrow^* z, z \triangleright t)\}$, with x, y , and z variables, and $\mathbb{T} = \{\text{Th}_{\rightarrow}, \text{Th}_{\rightarrow^*}, \text{Th}_{\triangleright}\}$ such that $\text{Th}_{\rightarrow} = \text{Th}_{\rightarrow^*} = \overline{\mathcal{R}}$ and $\text{Th}_{\triangleright}$ is given by:

$$(\forall x) x \triangleright x \quad (7) \quad (\forall x_1, \dots, x_k) f(x_1, \dots, x_k) \triangleright x_i \quad (9)$$

$$(\forall x, y, z) x \triangleright y \wedge y \triangleright z \Rightarrow x \triangleright z \quad (8) \quad \text{for each } f \in \mathcal{F} \text{ and } 1 \leq i \leq k$$

Example 4. A term t is *root-stable* (with respect to a TRS \mathcal{R}) if t cannot be reduced to a redex, i.e., there is no rule $\ell \rightarrow r \in \mathcal{R}$ such that $t \rightarrow^* \sigma(\ell)$ for some substitution σ . If \mathcal{R} consists of rules $\ell_1 \rightarrow r_1, \dots, \ell_p \rightarrow r_p$ (assume that different rules in \mathcal{R} share no variable), we can prove a *ground* term t root-stable by showing the $\{\text{Th}_{\rightarrow^*}\}$ -infeasibility of $\mathcal{G} = \{(t \rightarrow^* \ell_1); \dots; (t \rightarrow^* \ell_p)\}$ with $\text{Th}_{\rightarrow^*} = \overline{\mathcal{R}}$.

More examples of theories Th_{\bowtie} can be found in [9, Sections 5.3 and 5.4] and [11, Sections 8.1 and 8.2].

Given theories Th, Th' and a set of atoms \mathbb{A} , we write $\text{Th} \equiv_{\mathbb{A}} \text{Th}'$ if for all $A \in \mathbb{A}$, $\text{Th} \vdash A$ if and only if $\text{Th}' \vdash A$. Also, given a set of atoms \mathbb{A} and a predicate symbol \bowtie , \mathbb{A}_{\bowtie} is the subset of atoms in \mathbb{A} with root \bowtie .

Definition 2. *Given a set of predicates \mathbb{P} and a \mathbb{P} -indexed set of theories \mathbb{T} , we say that a theory Th preserves a feasibility sequence \mathbf{F} (in \mathbb{T}) if for all predicates \bowtie occurring in \mathbf{F} , $\text{Th} \equiv_{\mathbb{A}_{\bowtie}} \text{Th}_{\bowtie}$ holds. Thus, Th cannot prove more atoms rooted with \bowtie than Th_{\bowtie} . Similarly, Th preserves a goal $\mathcal{G} = \{\mathbf{F}_i\}_{i=1}^m$ if it preserves \mathbf{F}_i for all $1 \leq i \leq m$.*

In the following, when no confusion arises, we do not explicitly mention the underlying set of theories \mathbb{T} . Given $\mathcal{G} = \{\mathbf{F}_i\}_{i=1}^m$ and $\mathbf{F}_i = (s_{ij} \bowtie_{ij} t_{ij})_{j=1}^{n_i}$ for $1 \leq i \leq m$, we let $\text{Th}_{\mathbf{F}_i} = \bigcup_{j=1}^{n_i} \text{Th}_{\bowtie_{ij}}$ and $\text{Th}_{\mathcal{G}} = \bigcup_{i=1}^m \text{Th}_{\mathbf{F}_i}$.

Example 5. It is not difficult to see that $\text{Th}_{\mathcal{G}}$ preserves \mathcal{G} in Example 3.

The following result provides a (first-order) provability perspective of feasibility.

Theorem 1. *1. A condition $\gamma = s \bowtie t$ is feasible iff $\text{Th}_{\bowtie} \vdash (\exists \mathbf{x}) s \bowtie t$ holds.*
2. If $\mathbf{F} = (s_i \bowtie_i t_i)_{i=1}^n$ is feasible, then $\text{Th}_{\mathbf{F}} \vdash (\exists \mathbf{x}) \bigwedge_{i=1}^n s_i \bowtie_i t_i$ holds. If $\text{Th}_{\mathbf{F}}$ preserves \mathbf{F} and $\text{Th}_{\mathbf{F}} \vdash (\exists \mathbf{x}) \bigwedge_{i=1}^n s_i \bowtie_i t_i$ holds, then \mathbf{F} is feasible.
3. If $\mathcal{G} = \{\mathbf{F}_i\}_{i=1}^m$, where $\mathbf{F}_i = (s_{ij} \bowtie_{ij} t_{ij})_{j=1}^{n_i}$ for some n_i , is feasible, then we have that $\text{Th}_{\mathcal{G}} \vdash (\exists \mathbf{x}) \bigvee_{i=1}^m \bigwedge_{j=1}^{n_i} s_{ij} \bowtie_{ij} t_{ij}$ holds. If $\text{Th}_{\mathcal{G}} \vdash (\exists \mathbf{x}) \bigvee_{i=1}^m \bigwedge_{j=1}^{n_i} s_{ij} \bowtie_{ij} t_{ij}$ holds and $\text{Th}_{\mathcal{G}}$ preserves \mathcal{G} , then \mathcal{G} is feasible. Also, if there is $1 \leq i \leq m$ such that $\text{Th}_{\mathbf{F}_i}$ preserves \mathbf{F}_i and $\text{Th}_{\mathbf{F}_i} \vdash (\exists \mathbf{x}) \bigwedge_{j=1}^{n_i} s_{ij} \bowtie_{ij} t_{ij}$ holds, then \mathcal{G} is feasible.

Sentences in Theorem 1 are *Existentially Closed Boolean Combinations of Atoms* (ECBCAs), i.e., formulas of the form $(\exists \mathbf{x}) \bigvee_{i=1}^m \bigwedge_{j=1}^{n_i} A_{ij}$, where A_{ij} are atoms and \mathbf{x} is the sequence of variables occurring in such atoms. We have investigated them in [9,11]. Requiring preservation is necessary for items (2) and (3) in Theorem 1.

Example 6. Let $\mathcal{R}_1 = \{a \rightarrow b \Leftarrow b \approx a\}$ and $\mathcal{R}_2 = \{b \rightarrow a\}$. With $\mathbb{P} = \{\rightarrow, \rightarrow^*\}$ and $\mathbb{T} = \{\text{Th}_{\rightarrow}, \text{Th}_{\rightarrow^*}\}$, where $\text{Th}_{\rightarrow} = \overline{\mathcal{R}}_1 = \{(\forall x) x \rightarrow^* x, (\forall x, y, z) x \rightarrow y \wedge y \rightarrow^* z \Rightarrow x \rightarrow^* z, b \rightarrow^* a \Rightarrow a \rightarrow b\}$ and $\text{Th}_{\rightarrow^*} = \overline{\mathcal{R}}_2 = \{(\forall x) x \rightarrow^* x, (\forall x, y, z) x \rightarrow y \wedge y \rightarrow^* z \Rightarrow x \rightarrow^* z, b \rightarrow a\}$, we have $\text{Th} = \overline{\mathcal{R}}_1 \cup \overline{\mathcal{R}}_2$ and $\text{Th} \vdash a \rightarrow b$. However, $a \rightarrow b$ is not \mathbb{T} -feasible because $\overline{\mathcal{R}}_1 \not\vdash a \rightarrow b$. Note that Th does not preserve $(a \rightarrow b)$.

Preservation is often achieved by distinguishing predicates describing different computations.

Example 7. Let $\overline{\mathcal{R}}'_1$ and $\overline{\mathcal{R}}'_2$ be theories for \mathcal{R}_1 and \mathcal{R}_2 in Example 6, where \rightarrow_1^* is used instead of \rightarrow^* in $\overline{\mathcal{R}}_1$ (but \rightarrow remains as it is) to yield $\overline{\mathcal{R}}'_1 = \{(\forall x) x \rightarrow_1^* x, (\forall x, y, z) x \rightarrow y \wedge y \rightarrow_1^* z \Rightarrow x \rightarrow_1^* z, b \rightarrow_1^* a \Rightarrow a \rightarrow b\}$ and \rightarrow_2 is used instead of \rightarrow in $\overline{\mathcal{R}}_2$ (and \rightarrow^* is still used) to yield $\overline{\mathcal{R}}'_2 = \{(\forall x) x \rightarrow^* x, (\forall x, y, z) x \rightarrow_2 y \wedge y \rightarrow^* z \Rightarrow x \rightarrow^* z, b \rightarrow_2 a\}$. With $\text{Th}'_{\rightarrow} = \overline{\mathcal{R}}'_1$ and $\text{Th}'_{\rightarrow^*} = \overline{\mathcal{R}}'_2$ (and $\mathbb{T}' = \{\text{Th}'_{\rightarrow}, \text{Th}'_{\rightarrow^*}\}$), we have that $\text{Th}' = \overline{\mathcal{R}}'_1 \cup \overline{\mathcal{R}}'_2$ preserves $\mathcal{G} = \{(a \rightarrow b)\}$ and $\text{Th}' \not\vdash a \rightarrow b$. By Theorem 1 $a \rightarrow b$, is not \mathbb{T}' -feasible, as expected.

Theorem 1 characterizes feasibility of a goal \mathcal{G} as provability of an ECBCA $\varphi_{\mathcal{G}}$. If $\varphi_{\mathcal{G}}$ is shown *unprovable*, we conclude infeasibility of \mathcal{G} . And, under appropriate preservation conditions, theorem proving can be used to conclude feasibility of a goal \mathcal{G} . However,

(un)provability of atoms is often undecidable. For instance, for TRSs \mathcal{R} , it is well-known that given ground terms s and t , it is in general undecidable whether s rewrites into t ; i.e., whether $\overline{\mathcal{R}} \vdash s \rightarrow^* t$ holds (as Post’s correspondence problem is a particular case, see, e.g., [22, Section 4.1]). Hence, feasibility of conditions, sequences and goals remains, in general, undecidable. In order to obtain automatic proofs of feasibility, it is often useful to proceed using a ‘divide-and-conquer’ strategy. In the following, we exploit this idea to define a practical framework to prove/disprove feasibility of goals.

4 Feasibility Framework

In [3], proofs of termination of TRSs proceed by transforming the so-called *DP problems* τ . A divide-and-conquer approach is applied by means of *processors* \mathbb{P} mapping a DP problem τ into a (possibly empty) set $\mathbb{P}(\tau)$ of DP problems $\{\tau_1, \dots, \tau_n\}$. DP problems τ_i returned by \mathbb{P} can now be treated independently by using other processors. In this way, a *DP proof tree* is built.

In our setting, we first define notions of *f-problem* and *f-processor*, and then show how to use them to (dis)prove feasibility.

Definition 3 (f-Problem and f-Processor). *Given a set of predicates \mathbb{P} , a \mathbb{P} -indexed theory \mathbb{T} , and a goal \mathcal{G} , a pair $\tau = (\mathbb{T}, \mathcal{G})$ is called an f-Problem. We say that τ is feasible if \mathcal{G} is \mathbb{T} -feasible; otherwise it is infeasible.*

An f-Processor \mathbb{P} is a partial function from f-Problems into sets of f-Problems. Alternatively, it can return “yes”. $\text{Dom}(\mathbb{P})$ represents the domain of \mathbb{P} , i.e., the set of f-Problems τ that \mathbb{P} is defined for.

Definition 4 (Soundness and Completeness). *Let \mathbb{P} be an f-Processor and $\tau \in \text{Dom}(\mathbb{P})$. We say that \mathbb{P} is*

- sound iff τ is feasible whenever either $\mathbb{P}(\tau) = \text{“yes”}$ or $\exists \tau' \in \mathbb{P}(\tau)$, such that τ' is feasible.
- complete iff τ is infeasible whenever $\mathbb{P}(\tau) \neq \text{“yes”}$ and $\forall \tau' \in \mathbb{P}(\tau)$, τ' is infeasible.

Feasibility problems can be proved or disproved by using a proof tree as follows (where inner nodes include the root of the tree unless it consists of a single node).

Definition 5 (Feasibility Proof Tree). *Let $\tau = (\mathbb{T}, \mathcal{G})$ be an f-Problem. A feasibility proof tree (FP Tree) \mathcal{T} for τ is a tree whose inner nodes are labeled with f-Problems and the leaves are labeled either with f-Problems, “yes” or “no”. The root of \mathcal{T} is labeled with τ and for every inner node n labeled with τ' , there is an f-Processor \mathbb{P} such that $\tau' \in \text{Dom}(\mathbb{P})$ and:*

1. if $\mathbb{P}(\tau') = \text{“yes”}$ then n has just one child, labeled with “yes”.
2. if $\mathbb{P}(\tau') = \emptyset$ then n has just one child, labeled with “no”.
3. if $\mathbb{P}(\tau') = \{\tau_1, \dots, \tau_k\}$ with $k > 0$, then n has k children labeled with the f-Problems τ_1, \dots, τ_k .

Theorem 2 (Feasibility Framework). *Let \mathcal{T} be a feasibility proof tree for $\tau_I = (\mathbb{T}, \mathcal{G})$. Then:*

1. if all leaves in \mathcal{T} are labeled with “no” and all involved f-Processors are complete for the f-Problems they are applied to, then \mathcal{G} is \mathbb{T} -infeasible.
2. if \mathcal{T} has a leaf labeled with “yes” and all f-Processors in the path from τ_I to the leaf are sound for the f-Problems they are applied to, then \mathcal{G} is \mathbb{T} -feasible.

In the following, we describe some sound and complete f-Processors. If no confusion arises, we use *processor* instead of *f-Processor*.

4.1 Splitting Processor

Our first processor decomposes a feasibility goal into its feasibility sequences.

Definition 6 (Splitting Processor). *Let $\tau = (\mathbb{T}, \mathcal{G})$ be an f -Problem. The processor P^{Spl} is given by $\mathsf{P}^{\text{Spl}}(\tau) = \{(\mathbb{T}, \{\mathbf{F}\}) \mid \mathbf{F} \in \mathcal{G}\}$.*

The proof of the following result is immediate by using Definitions 3 and 1.

Theorem 3. *Processor P^{Spl} is sound and complete.*

Example 8. Consider the following TRS \mathcal{R} [13, Example 9]:

$$\begin{array}{ll} a \rightarrow b & (10) \qquad f(x, x) \rightarrow c \qquad (12) \\ b \rightarrow a & (11) \end{array}$$

Following Example 4, we prove root-stability of $f(a, c)$ as the $\{\overline{\mathcal{R}}\}$ -infeasibility of $\mathcal{G} = \{(f(a, c) \rightarrow^* a); (f(a, c) \rightarrow^* b); (f(a, c) \rightarrow^* f(x, x))\}$. With P^{Spl} we start the proof of infeasibility of $\tau = (\{\overline{\mathcal{R}}\}, \mathcal{G})$ as follows: $\mathsf{P}^{\text{Spl}}(\tau) = \{\tau_1, \tau_2, \tau_3\}$, where $\tau_1 = (\{\overline{\mathcal{R}}\}, \{(f(a, c) \rightarrow^* a)\})$, $\tau_2 = (\{\overline{\mathcal{R}}\}, \{(f(a, c) \rightarrow^* b)\})$, and $\tau_3 = ((\{\overline{\mathcal{R}}\}, \{(f(a, c) \rightarrow^* f(x, x))\})$.

4.2 Provability Processor

Our next processor exploits Theorem 1 to use theorem proving in proofs of feasibility.

Definition 7 (Provability processor). *Let $\tau = (\mathbb{T}, \mathcal{G})$ be an f -Problem with $\mathcal{G} = \{\mathbf{F}\} \uplus \mathcal{G}'$ where $\mathbf{F} = (s_i \bowtie_i t_i)_{i=1}^n$. Processor P^{Prov} is given by*

$$\mathsf{P}^{\text{Prov}}(\tau) = \text{“yes” iff } \text{Th}_{\mathbf{F}} \vdash (\exists \mathbf{x}) \bigwedge_{i=1}^n s_i \bowtie_i t_i \text{ holds.}$$

Note that, whenever $n = 0$, i.e., $\mathbf{F} = ()$, then $\bigwedge_{i=1}^n s_i \bowtie_i t_i$ is true and $\mathsf{P}^{\text{Prov}}(\tau) = \text{“yes”}$.

Theorem 4. *Processor P^{Prov} is complete. If $\text{Th}_{\mathbf{F}}$ preserves \mathbf{F} , then it is sound.*

In `infChecker`, we use `Prover9` [18] as a backend to implement P^{Prov} .

Example 9. For \mathcal{R} in Figure 1, the feasibility goal \mathcal{G} (see file 903 in COPS):

$$\{(le(x, \min(y)) \rightarrow^* \text{false}, \min(y) \rightarrow^* x)\}$$

and the corresponding first-order formula:

$$(\exists x, y) le(x, \min(y)) \rightarrow^* \text{false} \wedge \min(y) \rightarrow^* x \qquad (13)$$

with $\tau_I = (\{\overline{\mathcal{R}}\}, \mathcal{G})$, we have $\mathsf{P}^{\text{Prov}}(\tau_I) = \text{“yes”}$ by using `Prover9` to prove (13) by resolution as follows²:

² For readability, the output is slightly pretty printed.


```

(11) exists x y (le(x,min(y)) ->* false) & (min(y) ->* x) [goal]
(12) x ->* x [assumption]
(13) -(x -> y) | -(y ->* z) | (x ->* z) [assumption]
(15) -(x -> y) | (le(z,x) -> le(z,y)) [assumption]
(22) le(x,0) -> false [assumption]
(23) min(cons(x,nil)) -> x [assumption]
(27) -(le(x,min(y)) ->* false) | -(min(y) ->* x) [deny(11)]
(48) le(x,0) ->* false [ur(13,22,12)]
(59) -(le(min(x),min(x)) ->* false) [resolve(27,12)]
(67) -(le(min(x),min(x)) -> y) | -(y ->* false) [resolve(59,13)]
(69) -(le(min(x),y) ->* false) | -(min(x) -> y) [resolve(67,15)]
(76) -(le(min(cons(x,nil)),x) ->* false) [resolve(69,23)]
(77) $F [resolve(76,48)]

```

Example 10. Consider the two rules TRS $\mathcal{R} = \{a \rightarrow c(b), b \rightarrow c(b)\}$. For $\overline{\mathcal{R}} = \{(14) - (18)\}$ and $\text{Th}_{\geq} = \{(19) - (21)\}$:

$$\begin{array}{lll}
(\forall x) x \rightarrow^* x & (14) & (\forall x) x \geq x & (19) \\
(\forall x, y, z) (x \rightarrow y \wedge y \rightarrow^* z \Rightarrow x \rightarrow^* z) & (15) & (\forall x, y, z) x \geq y \wedge y \geq z \Rightarrow x \geq z & (20) \\
(\forall x, y) (x \rightarrow y \Rightarrow c(x) \rightarrow c(y)) & (16) & (\forall x) c(x) \geq x & (21) \\
a \rightarrow c(b) & (17) & & \\
b \rightarrow c(b) & (18) & &
\end{array}$$

`infChecker` can prove loopingness of \mathcal{R} as the feasibility of $(\{\overline{\mathcal{R}}, \text{Th}_{\geq}\}, \mathcal{G})$ by relying on `Prover9` with $\mathcal{G} = \{(x \rightarrow y, y \rightarrow^* z, z \geq x)\}$ (see Example 1). Note that the union of $\overline{\mathcal{R}}$ and Th_{\geq} preserves both $\overline{\mathcal{R}}$ and Th_{\geq} , as required for soundness of P^{Prov} .

If no proof of $\varphi_{\text{F}} = (\exists \mathbf{x}) \bigwedge_{i=1}^n s_i \bowtie_i t_i$ is found, then P^{Prov} does *not* apply. In this case, it is still possible that F is feasible, but the proof system failed to prove it. Also, it is possible that F is infeasible. In this case, our next processor, which tries to prove infeasibility as satisfiability [9], can be useful.

4.3 Satisfiability Processors

The next processor implements the satisfiability approach in [9].

Definition 8 (Satisfiability Processor). Let $\tau = (\mathbb{T}, \mathcal{G})$ be an *f-Problem* with $\mathcal{G} = \{\text{F}\} \uplus \mathcal{G}'$ for $\text{F} = (s_i \bowtie_i t_i)_{i=1}^n$ and \mathcal{A} be a structure. Processor P^{Sat} is given by $\text{P}^{\text{Sat}}(\tau) = (\mathbb{T}, \mathcal{G}')$ iff $\mathcal{A} \models \text{Th}_{\text{F}} \cup \{-(\exists \mathbf{x}) \bigwedge_{i=1}^n s_i \bowtie_i t_i\}$.

Remark 2. In the following, the soundness and completeness theorems given for the different introduced processors assume the notations previously introduced in the corresponding definitions.

In the following, we say that a theory Th is *stable* if for all terms s, t and substitutions σ , if $\text{Th} \vdash s \bowtie t$, then $\text{Th} \vdash \sigma(s) \bowtie \sigma(t)$.

Theorem 5. Processor P^{Sat} is sound. If $\mathcal{T}(\mathcal{F}) \neq \emptyset$ and Th_{F} is stable, then it is complete.

In `infChecker`, we use the model generators `AGES` [5] and `Mace4` [18] to find suitable structures \mathcal{A} to be used in the implementation of P^{Sat} .

Example 11. For \mathcal{R} , $\overline{\mathcal{R}}$ and Th_{\supseteq} as in Example 10, we can prove term a non-looping. The following structure over $\mathbb{N} \cup \{-1\}$:

$$\begin{array}{lll} a^{\mathcal{A}} = -1 & b^{\mathcal{A}} = 1 & c^{\mathcal{A}}(x) = x \\ x \rightarrow^{\mathcal{A}} y \Leftrightarrow x \leq 1 \wedge y \geq 1 & x (\rightarrow^*)^{\mathcal{A}} y \Leftrightarrow x \leq y & x \supseteq^{\mathcal{A}} y \Leftrightarrow x \leq y \end{array}$$

satisfies $\overline{\mathcal{R}} \cup \text{Th}_{\supseteq} \cup \{\neg(\exists x, y) (a \rightarrow x \wedge x \rightarrow^* y \wedge y \supseteq a)\}$. Thus, a is non-looping.

The following version of P^{Sat} often provides a direct answer about infeasibility of a goal.

Definition 9 (One-Step Satisfiability Processor). Let $\tau = (\mathbb{T}, \mathcal{G})$ be an f -Problem with $\mathcal{G} = \{F_1; \dots; F_m\}$, where, for all $1 \leq i \leq m$, F_i is $(s_{i1} \bowtie_{i1} t_{i1}, \dots, s_{in_i} \bowtie_{in_i} t_{in_i})$ for some $n_i > 0$. Let \mathcal{A} be a structure. Processor $\mathsf{P}^{\text{SatAll}}$ is given by $\mathsf{P}^{\text{SatAll}}(\tau) = \emptyset$ iff $\mathcal{A} \models \text{Th}_{\mathcal{G}} \cup \{\neg(\exists \mathbf{x}) \bigvee_{i=1}^m \bigwedge_{j=1}^{n_i} s_{ij} \bowtie_{ij} t_{ij}\}$.

Theorem 6. Processor $\mathsf{P}^{\text{SatAll}}$ is sound. If $\mathcal{T}(\mathcal{F}) \neq \emptyset$ and $\text{Th}_{\mathcal{G}}$ is stable, then it is complete.

Example 12. (continuing Example 8) With `Mace4` we obtain a model of

$$\overline{\mathcal{R}} \cup \{\neg(\exists x) (f(a, c) \rightarrow^* a \vee f(a, c) \rightarrow^* b \vee f(a, c) \rightarrow^* f(x, x))\}$$

Since $\mathsf{P}^{\text{SatAll}}(\tau) = \emptyset$, \mathcal{G} is \mathbb{T} -infeasible. This is an alternative proof without P^{Spl} .

Although $\mathsf{P}^{\text{SatAll}}$ can be simulated by a single step of P^{Spl} followed by the application of P^{Sat} to the obtained f -Problems, from a practical point of view $\mathsf{P}^{\text{SatAll}}$ has the advantage of avoiding the overloading due to the split the initial goal into a set of sequences with the generation of several models by means of calls to (external) model generators. Instead, $\mathsf{P}^{\text{SatAll}}$ makes a single call to the model generator(s).

4.4 Usable Rules in CTRS Theories

As discussed in [13, Section 2], dealing with CTRSs $\mathcal{R} = (\mathcal{F}, R)$, we may often drop some rules in R before establishing (in)feasibility of conditions $s \rightarrow^* t$. First, consider the following overapproximation of the set of rules that can be applied to a term t :

$$\text{RULES}(\mathcal{R}, t) = \{\ell \rightarrow r \Leftarrow c \in R \mid \exists p \in \mathcal{P}os(t), \text{root}(\ell) = \text{root}(t|_p)\}$$

The set of *usable rules* for t is defined as follows:

$$\mathcal{U}(\mathcal{R}, t) = \text{RULES}(\mathcal{R}, t) \cup \bigcup_{l \rightarrow r \Leftarrow c \in \text{RULES}(\mathcal{R}, t)} \left(\mathcal{U}(\mathcal{R}^{\sharp}, r) \cup \bigcup_{s \approx t \Leftarrow c} \mathcal{U}(\mathcal{R}^{\sharp}, s) \right)$$

where $\mathcal{R}^{\sharp} = \mathcal{R} - \text{RULES}(\mathcal{R}, t)$.³ Given a sequence \mathbf{F} , we let

$$\mathcal{U}_{\rightarrow^*}(\mathcal{R}, \mathbf{F}) = \bigcup_{s \rightarrow^* t \in \mathbf{F}} \mathcal{U}(\mathcal{R}, s) \quad (22)$$

³ The use of \mathcal{R}^{\sharp} instead of \mathcal{R} is important for implementing the computation of usable rules. By decreasing (from \mathcal{R} to \mathcal{R}^{\sharp}) the set of considered rules, the recursive definition is shown to be terminating.

Example 13. For \mathcal{R} in Figure 1 and $F = (le(0, s(0)) \rightarrow^* x)$, $\mathcal{U}_{\rightarrow^*}(\mathcal{R}, F) = \{(1), (2), (3)\}$.

Let $\overline{\mathcal{R}}|_{\mathcal{U}_{\rightarrow^*}(\mathcal{R}, F)}$ be the first-order theory for the CTRS $(\mathcal{F}, \mathcal{U}_{\rightarrow^*}(\mathcal{R}, F))$, which keeps the original signature of \mathcal{R} .

Definition 10. Let $\tau = (\mathbb{T}, \mathcal{G})$ be an *f-Problem* such that $\mathbb{T} = \{\text{Th}_{\rightarrow^*}\} \uplus \mathbb{T}'$ with $\text{Th}_{\rightarrow^*} = \overline{\mathcal{R}}$ for a CTRS \mathcal{R} and $\mathcal{G} = \{F\} \uplus \mathcal{G}'$. Let $\text{Th}'_{\rightarrow^*} = \overline{\mathcal{R}}|_{\mathcal{U}_{\rightarrow^*}(\mathcal{R}, F)}$. Processor P^{UR} is given by $\text{P}^{\text{UR}}(\tau) = \{(\{\text{Th}'_{\rightarrow^*}\} \uplus \mathbb{T}', \{F\}), (\mathbb{T}, \mathcal{G}')\}$.

P^{UR} distributes the sequences in \mathcal{G} in two new *f-Problems*: the first one consists of a goal with a single sequence F together with a refined version of \mathbb{T} where $\overline{\mathcal{R}}$ is simplified into $\overline{\mathcal{R}}|_{\mathcal{U}_{\rightarrow^*}(\mathcal{R}, F)}$; the second *f-Problem* consists of \mathcal{G}' but keeps the original set of theories \mathbb{T} . By using [13, Proposition 4], we can see that the $(\{\overline{\mathcal{R}}|_{\mathcal{U}_{\rightarrow^*}(\mathcal{R}, F)}\} \uplus \mathbb{T})$ -feasibility of a sequence F implies its $(\{\overline{\mathcal{R}}\} \uplus \mathbb{T})$ -feasibility. Similarly, $(\{\overline{\mathcal{R}}\} \uplus \mathbb{T})$ -infeasibility of F can be proved as $(\{\overline{\mathcal{R}}|_{\mathcal{U}_{\rightarrow^*}(\mathcal{R}, F)}\} \uplus \mathbb{T})$ -infeasibility provided that all terms s in feasibility conditions $s \rightarrow^* t$ in F are ground. Thus, we have the following:

Theorem 7. P^{UR} is sound. If for all $s \rightarrow^* t \in F$, s is ground, then P^{UR} is complete.

As discussed in the last paragraph of [13, Section 2], the groundness requirement cannot be dropped, in general (even for TRSs).

Example 14. For $\mathcal{R} = \{a \rightarrow b\}$, the sequence $F = (x \rightarrow^* a, x \rightarrow^* b)$ is $\{\text{Th}_{\rightarrow^*}\}$ -feasible (just instantiate variable x to a and use the rule in \mathcal{R}). However, it is *not* $\{\text{Th}'_{\rightarrow^*}\}$ -feasible for $\text{Th}'_{\rightarrow^*} = \overline{\mathcal{R}}|_{\mathcal{U}_{\rightarrow^*}(\mathcal{R}, F)}$ because $\mathcal{U}(\mathcal{R}, x)$ is empty. Hence, $\mathcal{U}_{\rightarrow^*}(\mathcal{R}, F) = \mathcal{U}(\mathcal{R}, x) \cup \mathcal{U}(\mathcal{R}, x)$ is also empty.

P^{UR} deals with many-step conditions $s \rightarrow^* t$ only. Furthermore, note that no change (simplification) in Th_{\rightarrow} (if used in \mathbb{T}) is introduced. For one-step conditions $s \rightarrow t$, we can use a similar (sound and complete) processor P^{UR1} as follows. Let $\mathcal{U}_{\rightarrow}(\mathcal{R}, F) = \bigcup_{s \rightarrow t \in F} \mathcal{U}(\mathcal{R}, s)$ and $\overline{\mathcal{R}}|_{\mathcal{U}_{\rightarrow}(\mathcal{R}, F)}$ be the first-order theory for $(\mathcal{F}, \mathcal{U}_{\rightarrow}(\mathcal{R}, F))$.

Definition 11. Let $\tau = (\mathbb{T}, \mathcal{G})$ be an *f-Problem* such that $\mathbb{T} = \{\text{Th}_{\rightarrow}\} \uplus \mathbb{T}'$ with $\text{Th}_{\rightarrow} = \overline{\mathcal{R}}$ for a CTRS \mathcal{R} and $\mathcal{G} = \{F\} \uplus \mathcal{G}'$. Let $\text{Th}'_{\rightarrow} = \overline{\mathcal{R}}|_{\mathcal{U}_{\rightarrow}(\mathcal{R}, F)}$. Processor P^{UR1} is given by $\text{P}^{\text{UR1}}(\tau) = \{(\{\text{Th}'_{\rightarrow}\} \uplus \mathbb{T}', \{F\}), (\mathbb{T}, \mathcal{G}')\}$.

Theorem 8. P^{UR1} is sound. If for all $s \rightarrow t \in F$, s is ground, then P^{UR1} is complete.

Starting from the *f-Problem* $(\mathbb{T}, \mathcal{G})$, where $\mathcal{G} = \{F\} \uplus \mathcal{G}'$, both P^{UR} and P^{UR1} return two *f-Problems* $(\mathbb{T}_1, \mathcal{G}_1)$ and $(\mathbb{T}_2, \mathcal{G}_2)$. For both P^{UR} and P^{UR1} , we have $\mathcal{G}_1 = \{F\}$, $\mathcal{G}_2 = \mathcal{G}'$, and $\mathbb{T}_2 = \mathbb{T}$. As for \mathbb{T}_1 , P^{UR} changes the component $\text{Th}_{\rightarrow^*}$ of \mathbb{T} . On the other hand, P^{UR1} changes Th_{\rightarrow} . With regard to the preservation property, which is relative to the goal and theory in a given *f-Problem*, whenever it holds for $(\mathbb{T}, \mathcal{G})$, it is not difficult to see (from the definition of preservation and usable rules) that it also remains true for $(\mathbb{T}_1, \mathcal{G}_1)$ and $(\mathbb{T}_2, \mathcal{G}_2)$.

4.5 Narrowing on Rewriting Conditions Processor

Reachability problems $\sigma(s) \rightarrow^* \sigma(t)$ are often investigated using *narrowing* and unification conditions directly over terms s and t , thus avoiding the ‘generation’ of the required substitution σ . In this section, we use narrowing to simplify feasibility conditions in \mathcal{G} . Definition 12 describes how narrowing is defined in the context of CTRSs. In the following, we write $s =_{\theta}^? t$ if s and t unify with *mgu* θ .

Definition 12. [16, Definition 79] *Let \mathcal{R} be a CTRS. A term s narrows to a term t (written $s \rightsquigarrow_{\mathcal{R}, \theta, p} t$ or just $s \rightsquigarrow_{\mathcal{R}, \theta} t$ or even $s \rightsquigarrow t$), iff there is a nonvariable position $p \in \text{Pos}_{\mathcal{F}}(s)$, a renamed rule $\ell \rightarrow r \leftarrow s_1 \rightarrow t_1, \dots, s_n \rightarrow t_n$ in \mathcal{R} , substitutions $\theta_0, \dots, \theta_n, \tau_1, \dots, \tau_n$, and terms t'_1, \dots, t'_n such that:*

1. $s|_p \stackrel{?}{=}_{\theta_0} \ell$,
2. for all i , $1 \leq i \leq n$, $\eta_{i-1}(s_i) \rightsquigarrow_{\mathcal{R}, \theta_i}^* t'_i$ and $t'_i \stackrel{?}{=}_{\tau_i} \theta_i(\eta_{i-1}(t_i))$, where $\eta_0 = \theta_0$ and for all $i > 0$, $\eta_i = \tau_i \circ \theta_i \circ \eta_{i-1}$, and
3. $t = \theta(s[r]_p)$, where $\theta = \eta_n$.

We write $u \rightsquigarrow_{\mathcal{R}, \beta}^* v$ for terms u, v and substitution β iff there are terms u_1, \dots, u_{m+1} and substitutions β_1, \dots, β_m for some $m \geq 0$ such that

$$u = u_1 \rightsquigarrow_{\mathcal{R}, \beta_1} u_2 \rightsquigarrow_{\mathcal{R}, \beta_2} \dots \rightsquigarrow_{\mathcal{R}, \beta_m} u_{m+1} = v$$

and $\beta = \beta_m \circ \dots \circ \beta_1$ (or $\beta = \varepsilon$ if $m = 0$).

Given a term u , the set $N_1(\mathcal{R}, u)$ represents the set of one-step \mathcal{R} -narrowings issued from u :

$$N_1(\mathcal{R}, u) = \{(v, \theta \downarrow_{\text{Var}(u)}) \mid u \rightsquigarrow_{\ell \rightarrow r \leftarrow c, \theta} v, \ell \rightarrow r \leftarrow c \in \mathcal{R}\} \quad (23)$$

where $\theta \downarrow_{\text{Var}(u)}$ is a substitution defined by $\theta \downarrow_{\text{Var}(u)}(x) = \theta(x)$ if $x \in \text{Var}(u)$ and $\theta \downarrow_{\text{Var}(u)}(x) = x$ otherwise.

As discussed in [16, Section 7.5], the set $N_1(\mathcal{R}, u)$ can be *infinite*. In [16, Proposition 87] some sufficient conditions for finiteness of $N_1(\mathcal{R}, u)$ are given. Knowing these restrictions, in Theorem 9 we define a narrowing processor on feasibility conditions.

Given a sequence $F_k = (s_j \bowtie_j t_j)_{j=1}^n$ in a goal \mathcal{G} , and $1 \leq i \leq n$ such that $\bowtie_i = \rightarrow^*$, $\overline{\mathcal{N}}(\mathcal{R}, \mathcal{G}, k, i)$ returns a new set of feasibility goals where each element of the set corresponds to a possible narrowing on the condition i :

$$\overline{\mathcal{N}}(\mathcal{R}, \mathcal{G}, k, i) = \{\mathcal{G}[\mathbf{F}_k[\boldsymbol{\theta}, w \rightarrow^* t_i]_i]_k \mid s_i \rightarrow^* t_i \in F_k, (w, \boldsymbol{\theta}) \in N_1(\mathcal{R}, s_i)\}$$

where $\boldsymbol{\theta}$ consists of new conditions $x_1 \rightarrow^* \theta(x_1), \dots, x_m \rightarrow^* \theta(x_m)$ obtained from the bindings in θ for variables in $\text{Var}(s_i) = \{x_1, \dots, x_m\}$.

Definition 13 (Narrowing on f-Conditions Processor). *Let $\tau = (\{\overline{\mathcal{R}}\}, \mathcal{G})$ be an f-Problem, $s_i \rightarrow^* t_i \in F_k$ for some F_k in \mathcal{G} , and $\mathcal{N} \subseteq \overline{\mathcal{N}}(\mathcal{R}, \mathcal{G}, k, i)$ finite. \mathbf{P}^{NC} is given by $\mathbf{P}^{\text{NC}}(\tau) = \{(\{\overline{\mathcal{R}}\}, \mathcal{G}') \mid \mathcal{G}' \in \mathcal{N}\}$.*

Given a term s , we let $\text{NRules}(\mathcal{R}, s)$ be the set of rules $\alpha : \ell \rightarrow r \leftarrow c \in \mathcal{R}$ such that a nonvariable subterm t of s is a *narrex* of α ,⁴ and, given a substitution θ , we denote as $\theta \downarrow_{\text{Var}(s)}$ the substitution defined by $\theta \downarrow_{\text{Var}(s)}(x) = \theta(x)$ if $x \in \text{Var}(s)$ and $\theta \downarrow_{\text{Var}(s)}(x) = x$ otherwise.

Theorem 9. \mathbf{P}^{NC} *is sound. If $\mathcal{N} = \overline{\mathcal{N}}(\mathcal{R}, \mathcal{G}, k, i)$ and $s_i \rightarrow^* t_i \in F_k$ is such that s_i and t_i do not unify and either s_i is ground and \mathcal{R} is a 2-CTRS or (1) $\text{NRules}(\mathcal{R}, s_i)$ is a TRS, (2) s_i is linear, and (3) $\text{Var}(s_i) \cap \text{Var}(t_i) = \emptyset$, then \mathbf{P}^{NC} is complete.*⁵

⁴ Given a CTRS \mathcal{S} , a non-variable term t is a *narrowing redex* (or a *narrex*, for short) of a rule $\ell \rightarrow r \leftarrow s_1 \approx t_1, \dots, s_n \approx t_n \in \mathcal{S}$ if t and ℓ unify with *mgu* θ (we assume $\text{Var}(t) \cap \text{Var}(\ell) = \emptyset$). However, if $(\theta(s_1) \approx \theta(t_1), \dots, \theta(s_n) \approx \theta(t_n))$ can be proved $\{\overline{\mathcal{S}}\}$ -infeasible, we can discard t , as no narrowing step is possible on it. In our current implementation, though, only the unification test is used.

⁵ This processor is inspired by the processor defined in [17, Section 4.1]. A justification for the completeness conditions can be obtained from [17, Examples 18 and 19].

$$\begin{aligned}
& \text{add}(0, x) \rightarrow x \\
& \text{add}(s(x), y) \rightarrow s(\text{add}(x, y)) \\
& \text{div}(0, s(x)) \rightarrow 0 \\
& \text{div}(s(x), s(y)) \rightarrow 0 \Leftarrow \text{lte}(s(x), y) \approx \text{true} \\
& \text{div}(s(x), s(y)) \rightarrow s(q) \Leftarrow \text{lte}(s(x), y) \approx \text{false}, \text{div}(\text{minus}(x, y), s(y)) \approx q \\
& \quad \text{lte}(0, y) \rightarrow \text{true} \\
& \quad \text{lte}(s(x), 0) \rightarrow \text{false} \\
& \quad \text{lte}(s(x), s(y)) \rightarrow \text{lte}(x, y) \\
& \text{minus}(0, s(y)) \rightarrow 0 \\
& \text{minus}(s(x), s(y)) \rightarrow \text{minus}(x, y) \\
& \quad \text{minus}(x, 0) \rightarrow x \\
& \quad \text{mod}(0, y) \rightarrow 0 \\
& \quad \text{mod}(x, 0) \rightarrow x \\
& \text{mod}(x, s(y)) \rightarrow \text{mod}(\text{minus}(x, s(y)), s(y)) \Leftarrow \text{lte}(s(y), x) \approx \text{true} \\
& \text{mod}(x, s(y)) \rightarrow x \Leftarrow \text{lte}(s(y), x) \approx \text{false} \\
& \quad \text{mult}(0, y) \rightarrow 0 \\
& \text{mult}(s(x), y) \rightarrow \text{add}(\text{mult}(x, y), y) \\
& \quad \text{power}(x, 0) \rightarrow s(0) \\
& \quad \text{power}(x, n) \rightarrow \text{mult}(\text{mult}(y, y), s(0)) \Leftarrow n \approx s(n'), \\
& \quad \quad \quad \text{mod}(n, s(s(0))) \approx 0, \text{power}(x, \text{div}(n, s(s(0)))) \approx y \\
& \quad \text{power}(x, n) \rightarrow \text{mult}(\text{mult}(y, y), x) \Leftarrow n \approx s(n'), \\
& \quad \quad \quad \text{mod}(n, s(s(0))) \approx s(z), \text{power}(x, \text{div}(n, s(s(0)))) \approx y
\end{aligned}$$
Fig. 3. CTRS 529.trrs in COPS

Even with $\overline{\mathcal{N}}(\mathcal{R}, \mathcal{G}, k, i)$ infinite, a subset \mathcal{N} of $\overline{\mathcal{N}}(\mathcal{R}, \mathcal{G}, k, i)$ can be sufficient to prove feasibility. However, to prove infeasibility we need to consider all possible narrowings.

Example 15. Consider the CTRS \mathcal{R} in Figure 3, $\mathcal{G} = \{\text{lte}(s(x), 0) \rightarrow^* \text{true}\}$, and $\tau_I = (\{\overline{\mathcal{R}}\}, \mathcal{G})$. Since $\text{NRules}(\mathcal{R}, \text{lte}(s(x), 0))$ contains the *lte* rules only, $\overline{\mathcal{N}}(\mathcal{R}, \mathcal{G}, 1, 1) = \{(x \rightarrow^* x, \text{false} \rightarrow^* \text{true})\}$. Therefore, $\mathbf{P}^{\text{NC}}(\tau_I) = \{\tau_1\}$ with $\tau_1 = (\{\overline{\mathcal{R}}\}, (x \rightarrow^* x, \text{false} \rightarrow^* \text{true}))$. Since $\text{NRules}(\mathcal{R}, \text{lte}(s(x), 0))$ is a TRS, $\text{lte}(s(x), 0)$ is linear, and $\text{Var}(\text{lte}(s(x), 0)) \cap \text{Var}(\text{true}) = \emptyset$, \mathbf{P}^{NC} is complete. Now, we apply \mathbf{P}^{Sat} to τ_1 to obtain $\mathbf{P}^{\text{Sat}}(\tau_1) = \emptyset$ by using *Mace4*. The obtained FP tree is in displayed in Figure 4.

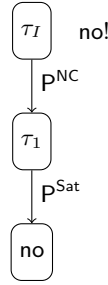
5 Implementation and Web Interface

infChecker 1.0 is written in *Haskell* and consists of 30 *Haskell* modules with more than 4500 lines of code. The tool can be used through its web interface here:

<http://zenon.dsic.upv.es/infChecker/>

The input format is an extended version of the Confluence Competition (CoCo) format [20], which is the official format used in the *infeasibility* (INF) category.⁶ The input has two components:

⁶ See <http://project-coco.uibk.ac.at/2019/categories/infeasibility.php>


Fig. 4. Proof tree obtained from Example 15

1. A CTRS \mathcal{R} in TPDB format⁷ which can specify a *replacement map* μ for *context-sensitive rewriting* (CSR [10]) establishing the arguments $\mu(f) \subseteq \{1, \dots, k\}$ which can be rewritten for each k -ary symbol f . This is top-down propagated to *positions* of terms, which are then called *active*. We write $s \hookrightarrow t$ if an *active* subterm of s can be rewritten so that $s \rightarrow t$. Then, \rightarrow^* , \downarrow , \leftrightarrow , etc. are generalized to CSR as \hookrightarrow^* , \downarrow , \leftrightarrow , etc., by using \hookrightarrow instead of \rightarrow .
2. An f-goal built using the set

$$\mathbb{P}_{\text{Ch}} = \{\mid>, \mid>=\} \cup \{->, ->*, -><-, <-->, (<-->*)\} \cup \{\backslash->, \backslash->*, \backslash-><-/, <-/\backslash->, <-/\backslash->*\} \cup \{==\}$$

of (binary) predicates for (strict) subterm ($\mid>$ and $\mid>=$), one or many rewriting steps ($->$ and $->*$), joinability ($-><-$), symmetric closure of $->$ ($<-->$), conversion ($<-->*$) and their context-sensitive versions $\backslash->$, $\backslash->*$, $\backslash-><-/$, $<-/\backslash->$, and $<-/\backslash->*$.

Theories Th_{\bowtie} for each $\bowtie \in \mathbb{P}_{\text{Ch}}$ are automatically obtained from the components of \mathcal{R} (signature, replacement map, conditional rules). Symbol $==$ is borrowed from the COPS syntax, where it is used to specify the conditional part of rewrite rules (see \approx in Figure 1). Both in the conditional part of rules and in f-goals, its meaning depends on the **CONDITIONTYPE** section of the input specifying how the conditions of rules are evaluated [22, Definition 7.1.3] according to:

CONDITIONTYPE	replace == by
ORIENTED	$->*$
JOIN	$-><-$
SEMI-EQUATIONAL	$<-->*$

In this respect, when using \hookrightarrow or \hookrightarrow^* (i.e., $\backslash->$ or $\backslash->*$) in f-goals, the associated theories $\text{Th}_{\hookrightarrow}$ and $\text{Th}_{\hookrightarrow^*}$ are those obtained by evaluating the conditions $s_i \approx t_i$ in rules using \hookrightarrow^* , \downarrow , or \leftrightarrow^* , depending on the label **ORIENTED**, **JOIN**, or **SEMI-EQUATIONAL** specified in **CONDITIONTYPE**. If no replacement map has been specified (i.e., no **STRATEGY** section with **CONTEXTSENSITIVE** label is given, the *trivial* replacement map $\mu_{\top}(f) = \{1, \dots, k\}$, establishing no replacement restrictions, is automatically assumed for each k -ary symbol f .

⁷ See http://zenon.dsic.upv.es/muterm/?page_id=31

When the problem is introduced, a model generator for infeasibility (**AGES**, **Mace4** or **Automatic**) can be selected. Then, pressing button **Prove automatically** initiates procedure to check whether the problem is feasible or infeasible in the given timeout.

Currently, **infChecker** implements the construction of the FP tree in Definition 5 with the processors presented in Section 4 by depth-first generation of the nodes and orderly attempting the following sequence of processors to develop each node:⁸ P^{Spl} , P^{Prov} , P^{Sat} , and P^{NC} . If the final answer is **YES** or **NO**, the tool displays a report in plain text. Otherwise, **MAYBE** is returned.

6 Experimental Evaluation

We participated in the INF category of the 2019 Confluence Competition (CoCo),⁹ with a limit of 60 seconds to return a proof of feasibility or infeasibility (or a *don't know* answer). **infChecker** obtained the following results:

INF Tool	Yes	No	Total
infChecker	40	32	72
ConCon	31	0	31
nonreach	30	0	30
Moca	26	0	26
maedmax	15	0	15
CO3	12	0	12

Answers *Yes/No* in the table refer to *infeasibility* (which is the focus of the competition). In our setting, given a CTRS \mathcal{R} and an infeasibility problem given as a feasibility sequence \mathcal{G} , we just return “*Yes*” if τ_I is proved infeasible, and “*No*” if τ_I is proved feasible. Apart from the 32 “*No*” answers, there are 7 more examples that can be proved positively (“*Yes*”) using **infChecker** only. There also are 10 examples that can be proved by other tools and cannot be proved by **infChecker**.

In the experiments P^{UR} was used 11 times and P^{NC} was used twice. We required a combination of processors in 13 examples: the sum of uses of P^{UR} and P^{NC} . Being unable to provide a definite (YES/NO) answer, their use always requires another processor to finish the proof. According to the strategy described at the end of Section 5, such a combination is necessary. Thus, we need both P^{UR} and P^{NC} to solve the examples.

7 Related Work

The notion of (in)feasibility of a logic formula has been investigated in [12, Section 4.1], in the context of the analysis of operational termination of programs in general logics [15]. A satisfiability approach to prove infeasibility of first-order formulas with respect to an order-sorted first-order theory [4] is described in [12, Section 4.1.1]. No attempt to

⁸ We use a Haskell library for parallelism. However, due to the *Breadth First Search* evaluation strategy of the library, in a parallel execution $P_1 \parallel P_2 \parallel \dots \parallel P_n$ of several processors we wait until the leftmost processor (P_1) is completely evaluated (returns a solution or reaches a timeout) before continuing with $P_2 \parallel \dots \parallel P_n$. Thus, there is a kind of ‘restricted’ parallelism in our implementation.

⁹ <http://project-coco.uibk.ac.at/2019/>

decompose such proofs by taking into account the structure of the logic formula (as done our feasibility framework) is made. No technique for proving feasibility is proposed. Actually, our feasibility framework could be advantageously used to implement proofs of operational termination of programs in general logics.

Sternagel and Yamada [25] define a framework to prove reachability constraints ϕ for TRSs \mathcal{R} as first-order formulas where only reachability atoms $s \rightarrow t$ (instead of $s \rightarrow^* t$) are allowed. As remarked in [25, footnote 1], negation and universal quantification are not considered, i.e., only ECBCAs with atoms $s \rightarrow t$ are (ultimately) considered. A constraint $s \rightarrow t$ is satisfied by a substitution σ with respect to \mathcal{R} if $\sigma(s) \rightarrow_{\mathcal{R}}^* \sigma(t)$. Reachability constraints ϕ are called satisfiable if there is a substitution σ such that $\sigma(\phi)$ ¹⁰ is satisfied in the usual first-order logic sense. Our approach is more flexible as more predicates can be defined by appropriate theories (including CTRSs). For instance, *non-root reachability* constraints $s \xrightarrow{\geq A} t$ (given in [25, Section 4] in terms of reachability constraints) can be defined by $\text{Th}_{\geq A^*}$ consisting of

$$(\forall x) x \xrightarrow{\geq A^*} x \quad (24)$$

$$(\forall x)(\forall y)(\forall z) x \xrightarrow{\geq A} y \wedge y \xrightarrow{\geq A^*} z \Rightarrow x \xrightarrow{\geq A^*} z \quad (25)$$

plus sentences $\overline{(\text{RI})}_\alpha$ for each rewrite rule α , sentences $\overline{(\text{C})}_{f,i}$ for each k -ary symbol f and $1 \leq i \leq k$, and $(\forall \mathbf{x})(\forall y_i)x_i \rightarrow y_i \Rightarrow f(x_1, \dots, x_i, \dots, x_k) \xrightarrow{\geq A} f(x_1, \dots, y_i, \dots, x_k)$. We could also cover CTRSs by also adding $\overline{(\text{T})}$, for the transitivity rule (T), necessary for the evaluation of the conditional part of conditional rules α (which may require root steps). Then, the non-reachability problems considered in [25, Section 4] for TRSs \mathcal{R} could be treated in our framework using $\mathbb{P} = \{\rightarrow, \rightarrow^*, \xrightarrow{\geq A^*}\}$ and $\mathbb{T} = \{\text{Th}_{\rightarrow}, \text{Th}_{\rightarrow^*}, \text{Th}_{\geq A^*}\}$, where $\text{Th}_{\rightarrow} = \text{Th}_{\rightarrow^*} = \overline{\mathcal{R}}$. Actually, we can ‘import’ the decomposition treatment for non-root reachability goals in [25, Definition 5] as a transformation processor (like P^{NC}) specific for non-root reachability conditions of TRSs as follows: let $\tau = (\mathbb{T}, \mathcal{G})$ and $F_i \in \mathcal{G}$ be such that $F_i = (\gamma_1, \dots, \gamma_j, \dots, \gamma_n)$ with $\gamma_j = f(s_1, \dots, s_k) \xrightarrow{\geq A^*} f(t_1, \dots, t_k) \in F_i$ for some terms s_i, t_i , $1 \leq i \leq k$. Then,

$$\text{P}^{\text{non-root-r}}(\tau) = \{(\mathbb{T}, \mathcal{G}[F'_i]_i)\}$$

where $F'_i = (\gamma_1, \dots, \gamma_{j-1}, s_1 \rightarrow^* t_1, \dots, s_k \rightarrow^* t_k, \dots, \gamma_{j+1}, \dots, \gamma_n)$ and $\mathcal{G}[F'_i]_i$ is the goal obtained by replacing the i -th sequence of \mathcal{G} by F'_i . This example also shows how techniques developed elsewhere could be smoothly integrated in our framework.

Decidability of reachability problems for *CSR* in TRSs (i.e., does $s \leftrightarrow^* t$ hold?) has been investigated using tree-automata techniques [1,6,7,8]. `infChecker` is able to (try to) prove and disprove reachability conditions as f-goals using predicate `\->*` without any specific restriction on the TRSs (e.g., left-linearity), as done in these papers.

Regarding the automation of proofs of infeasibility in (conditional) rewriting, 2019 was the first year the infeasibility category was included in the International Confluence Competition. The new category had a good reception, with 6 participating tools (summary of results in Section 6), and provided a good picture of the state of the art:

- CO3 tries to prove confluence and if it fails linearizes the condition and tries to compute a narrowing tree for the linearized condition. The applicability of narrowing trees in this context is restricted to *syntactically deterministic conditional*

¹⁰ obtained by (i) *renaming* all bounded variables in ϕ using variables not occurring in bindings of σ to obtain ϕ' , and then (ii) *replacing* each free variable x of ϕ' by $\sigma(x)$.

- term rewriting systems (right-hand sides of conditions must be constructor terms or ground normal forms) that are constructor systems [21].
- **ConCon** uses a variety of methods to check for infeasibility of conditional critical pairs, ranging from a simple technique based on unification, via symbol transition graph analysis, reachability problem decomposition, the exploitation of certain equalities in the conditions, and tree automata completion to equational reasoning [24].
 - **Moca** implements *maximal ordered completion* similar to **maedmax** [26] together with some *approximation techniques* not yet published.
 - **maedmax** implements *maximal order completion* [26].
 - **nonreach** uses two approaches: *transformations* based on decomposition and narrowing and *nonreachability checks* based on unification, symbol transition graphs, equational reasoning and tree automata completion [19,25].

Thanks to representing CTRSs \mathcal{R} as first-order theories $\overline{\mathcal{R}}$, **infChecker** was not only the most successful tool for checking infeasibility, but also the only tool currently able to *disprove* it (by proving feasibility). There also is room, however, for improvements, as witnessed by the 10 examples mentioned in the summary of experiments in Section 6. In order to deal with these examples (not handled by **infChecker**), **nonreach** uses *narrowing* and **Moca** uses satisfiability with LPO. Regarding **ConCon**, it is unclear from the report provided by the tool, which specific technique was used to solve the examples.

8 Conclusions and Future Work

We have extended and generalized the notion of *feasibility sequence* introduced in [13] by considering goals which are sets of sequences of conditions $s \bowtie t$ for arbitrary predicates \bowtie . Each predicate symbol \bowtie is ‘defined’ by a first-order theory Th_{\bowtie} . Such conditions, sequences, and goals have a precise logical characterization as ECBCAs, and its feasibility can be investigated as provability of such formulas (Theorem 1). We have shown some examples of properties (of CTRSs) which can be proved by using this approach. We have introduced a framework for proving and disproving feasibility of such goals. To the best of our knowledge, our logic-based notion of feasibility goal and the framework to prove and disprove them are new in the literature.

We have developed a new tool implementing our framework: **infChecker**. Currently, **infChecker** provides a first implementation of the framework introduced in this paper (restricted to CTRSs, but extended with context-sensitivity, subterm, etc.), and supports predicates like \rightarrow (one-step rewriting), \rightarrow^* (many-step rewriting), \downarrow (joinability), \leftrightarrow^* (conversion), and the analogous for context-sensitive rewriting. We also give support to \supseteq (subterm) and \triangleright (strict subterm). As far as we know, **infChecker** is the first tool dealing with (in)feasibility problems supporting this set of predicates. Also, the use of provability/satisfiability techniques in proofs of (in)feasibility seems to be new in the literature. We participated in the 2019 Confluence Competition [20] in the INF (infeasibility) category, being the most powerful tool for checking both infeasibility and feasibility. In the near future, we plan to extend **infChecker** to provide full support to our framework, by allowing the explicit definition of (not necessarily binary) predicates and independent first-order theories associated to such predicates besides the built-in set of predicates \mathbb{P}_{ICR} and associated theories which are available now.

Acknowledgments We thank the anonymous referees for many remarks and suggestions that led to improve the paper.

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