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A Wideband Diplexer for Ka-Band Passive Intermodulation Measurement

Abstract—In this paper we propose a novel Ka-band waveguide diplexer for passive intermodulation (PIM) measurements. Due to the higher operating frequency, this type of measurement setup usually requires wide band filters. In addition, due to the very large separation between satellite uplink and downlink bands, the PIM measurement diplexer unit must operate over a very wide frequency band. To satisfy such wide bandwidth requirements, the proposed topology is based on a band-reject filter for the downlink frequency band which is designed to provide, at the same time, the optimal passband performance for the downlink, and the optimal equiripple rejection over the PIM band. For the PIM channel, a band-pass filter combined with a high-pass section has been used. To validate the design, a prototype has been designed and manufactured. Very good agreement is shown between simulations and measurements, thereby fully validating the proposed solution.

Index Terms—Microwave filters, multiplexing, Ka-band, high-power filters, computer-aided engineering.

I. INTRODUCTION

The continuous demands for higher throughput in modern communication systems is pushing telecoms satellites to higher operating frequencies. Since the launch of the Thaicom-4 (IPSTAR) satellite in 2005, satellite operators have devoted continuous significant efforts to the development of higher bandwidth systems. In this context, the recently developed High Throughput Satellites (HTS) are exploiting the Ka-band frequency range [1]. The ITU (International Telecommunication Union) has, in fact, assigned a wide bandwidth for satellite applications at Ka-band, ranging from 17.3 GHz to 21.2 GHz for downlink and 27 GHz to 31 GHz for uplink, with a potential future use of the 21.4 GHz to 22 GHz band for additional downlink services [2].

New microwave components are therefore required at these frequency ranges. Furthermore, the hardware must be able to withstand the high operating power usually required by HTS systems, without generating spurious PIM signals that may degrade the system performance. As a consequence, the development of adequate PIM measurement setups is a fundamental requirement [3].

In this context, therefore, diplexers are a key building block. The typical configuration for wideband diplexers consists of a low-pass filter (downlink) and a high-pass filter (uplink) combined with a wideband junction. Diplexers of this type have been used for antenna applications in C-band (overall bandwidth about 50% with filters of 10-15% bandwidth) [4] and Ku-band (overall bandwidth close to 30%) [5]. For the even wider bandwidths of the extended C-band (filters around 20% relative bandwidth, and 65% overall for the whole diplexer), there are solutions based on the traditional configuration [6] or using ridge waveguide filters with a dual-ridge common port [7]. For Ka-band frequency range,

TABLE I
DESIGN SPECIFICATIONS FOR THE PIM KA-BAND DIPLEXER

Specification	Downlink channel	Uplink channel
Passband frequency	17.3–22 GHz	27–31 GHz
Return loss	> 20 dB	> 20 dB
Insertion loss	< 0.2 dB	< 1 dB
Isolation	> 50 dB	> 150 dB
Power handling	> 270 W CW	–

a conventional antenna diplexer is proposed in [8], although the channels do not extend over the whole band specified by the ITU [2].

In this paper, we present the design of a novel wideband diplexer to separate the high-power carriers (downlink band) from the PIM signal (uplink band), specifically suited for PIM measurement setups, covering the complete Ka-band ITU spectrum assignment for satellite applications. The novelty of the proposed topology is two-fold. First, a stub-based reject band filter for the downlink channel has been designed to provide, at the same time, the optimum passband performance, and the optimum equiripple rejection at the uplink (PIM) band. Second, a bandpass filter is also used, in conjunction with a standard high pass filter, to collect the PIM signals.

II. TOPOLOGY AND DESIGN CONSIDERATIONS

A. Design specifications

The specifications to be met by the PIM diplexer are listed in table I. The overall bandwidth is 58%, and the filters are required to have 24% and 14% bandwidth (according to ITU specifications at Ka-band). A diplexer of such unprecedented bandwidth requirements for Ka-band operation can not be found (to the authors knowledge) in the technical literature. In this context, it is important to note that, compared to antenna diplexer specifications, the rejection requirements for the uplink channel in the downlink band is much higher, although insertion loss requirements are somewhat relaxed.

Standard WR51 waveguides were required for the common and downlink ports, whereas a WR28 waveguide was required for the uplink port. Note that more than one mode propagates in the common port at the uplink band. For PIM measurements applications, the high-power carriers must travel from the common to the downlink port with small reflection and losses (to avoid heating), and without significant leakage to the uplink port (to avoid unwanted PIM masking). On the other hand, the input PIM signal, related to the fundamental mode, must be transmitted to the uplink port. In this context, it is important to note that what happens to the small part of the weak PIM signal coupled with higher other modes is not relevant for PIM measurements. However, as it will be shown later, the multimode behavior of the common port will indeed affect the measurements, as reported in Section III.

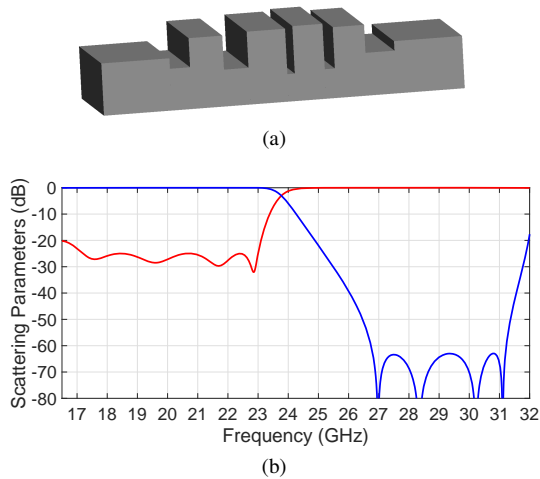


Fig. 1. Band reject filter topology in (a), and simulated response in (b).

B. Downlink (high-power carriers) filter

The filter used for the downlink channel is a band reject filter since there are no specification to be met below 17 GHz. The filter topology is a stub-based filter structure with the same width as the common port. Each stub provides a transmission zero in the rejection band, and a pole in the (lower) transmission band, which can be controlled independently. It is interesting to note that the function of the reject band is to provide a short circuit for the PIM signal, so that the PIM signal is reflected, and collected with the proper phasing, by the PIM branch of the diplexer. Although similar structures have already been proposed in the past [6], [9], [10], to the authors knowledge, the capabilities of the topology have not been fully exploited, since usually some of the parameters (stubs width/height or capacitive sections length/height) are set to the same (or, at best, stepped) value.

A filter composed of 9 elements was enough to satisfy the required specifications. The resulting topology is shown in Fig. 1a, and the simulated response in Fig. 1b. Note the optimum Zolotarev transfer function at the passband and equiripple response in the rejection band (with the four possible transmission zeros). The filter gap is above 2 mm, and the connection to the output port of 4.65 mm height (to cut-off the TE_{01} mode in the uplink band) did not require an input/output transformer.

C. PIM (Uplink) filter

Traditionally, in antenna diplexers, the uplink branch is implemented by means of a high-pass filter based on a narrowed waveguide section below cut-off at the downlink band [4], [5], [6], [8]. Although this is a simple topology, for our application we propose to include also a band-pass filter. This choice has the following important advantages:

- Improves compactness by replacing the input taper, and increases the rejection of the high-pass section.
- Facilitates the connection of the downlink and uplink filter to the diplexer junction, as the first resonator of the band-pass filter provides isolation at the downlink

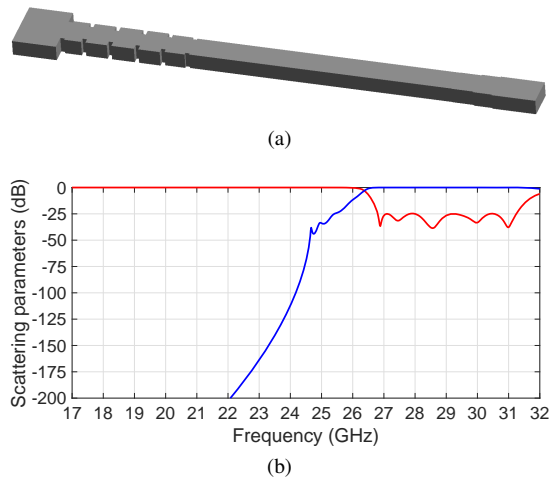


Fig. 2. PIM filter topology in (a), and simulated response in (b).

frequency band. This is particularly convenient in order to avoid unwanted spikes in wideband multiplexers [11].

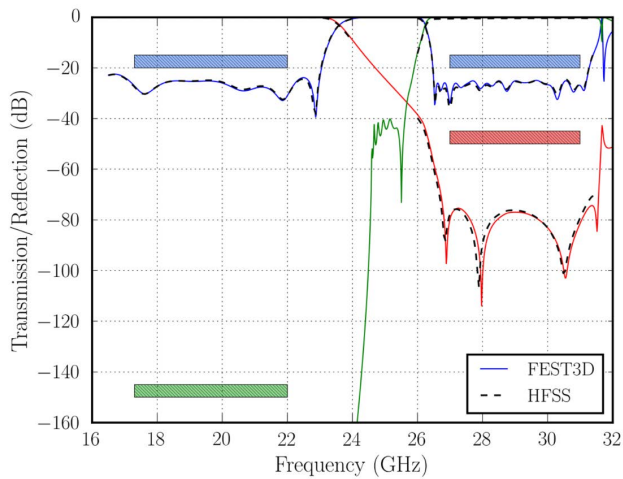
- Provides rejection above the uplink band, preventing the transmission of undesired signals at higher frequencies.

Fig. 2 shows the optimized uplink filter and the simulated response. It consists of a fifth order TE_{101} band-pass filter with WR28 waveguide resonators (the last one is 6.5 mm wide). A higher order filter was not considered appropriate, due to manufacturing accuracy limitations, since the diplexer was required to have no tuning screws to avoid PIM generation. The high-pass section has chosen to have a width of 6.1 mm. Finally, a two-step $\lambda/4$ taper was used to connect to the standard WR28 uplink (PIM) port. The band-pass filter provides 75 dB of the 200 dB rejection at 22 GHz and takes up 30 mm of the 103 mm overall length of the uplink filter. Using a high-pass section without band-pass filter, would have required about 125 mm. The size shrinkage could have been pushed further by reducing the width of the filter resonators to 6.1 mm. However, this possibility was not exploited in the design since we had no severe limitation in length or losses.

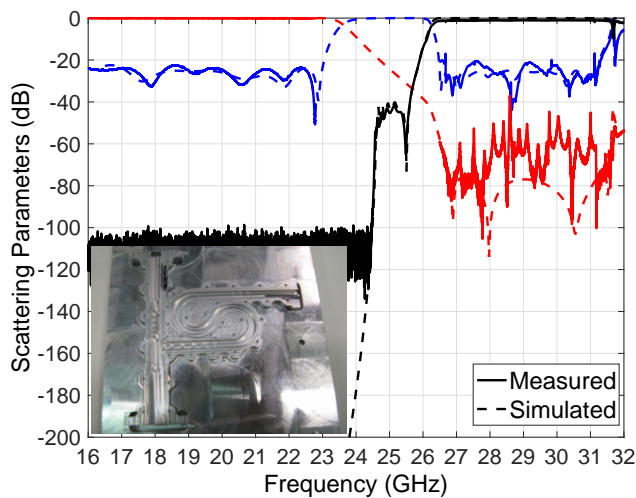
III. PIM DIPLEXER RESULTS

The filters for the downlink (input power) and the uplink (PIM) channel have been combined using a wideband E-plane T-junction, since it provides a higher bandwidth than H-plane ones, and also allows for low-PIM clam-shell manufacturing (with two identical halves), without interrupting current lines. The high-pass filter section has been meandered for compactness reasons while keeping the PIM port in a proper position. An additional section was added at the beginning of the low-pass filter to improve its adaptation to the diplexer junction. The design has been carried out using CST FEST3D software, as the topology can be analyzed efficiently and accurately with advanced modal methods.

The measured scattering parameters of the hardware for TE_{10} excitation at the common port are shown in Fig. 3a. The agreement between CST FEST3D and Ansys HFSS responses is indeed excellent, fully validating the complete design.



(a)



(b)

Fig. 3. Comparison of FEST3D simulated response with HFSS response (design masks included for reference) in (a), and with measurements in (b).

The diplexer has been manufactured in aluminum, without any tuning elements. The measured response is shown in Fig. 3b. The response in the monomode region of the WR51 input port agrees very well with the simulated response (the small deviations can be attributed to manufacturing tolerances). On the other hand, there are spikes in the isolation response between the common port and the downlink port in the uplink band. These spikes are related to the transmission path provided by the TE_{20} mode, which is excited by (even small) misalignment in the hardware. Fortunately, however, the spikes are not a problem for the intended application of the diplexer, and could be easily removed by properly adjusting the width of the band reject filter or the input port [6], at the expense of a somewhat poorer power-handling and insertion loss performances.

For the downlink band (input power), the measured return loss was better than 22.5 dB and the insertion losses lower than 0.15 dB. The simulated power-handling capability was

several dB above the requirements of 270 W. In the uplink band, the return loss satisfied the specification of 20 dB (see Table I), whereas the insertion loss was in the range between 0.45 and 0.6 dB due to the long meandered high-pass section. It is important to note that the use of a band-pass filter does reduce the size of the high-pass section, but at the cost of slightly increased insertion losses. Measurements carried out with a spectrum analyzer proved that the isolation between the common port and the uplink port was above 160 dB in the ITU downlink band.

IV. CONCLUSIONS

A novel wideband diplexer for PIM measurement setups at Ka-band has been proposed. The design has been optimized to cover the complete Ka-band ITU spectrum allocation. The structure contains a band-reject filter which has been designed for an optimized performance of both the downlink passband (input power), and the uplink rejection band (PIM). The diplexer also includes a band-pass filter in the high-pass section, to improve compactness and to simplify the common junction optimization. In addition to design guidelines, the simulated response obtained with several EM simulators has been presented. The EM simulations show very good agreement with the measured performance of an aluminum prototype, thereby fully validating the proposed topology.

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