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PAPER

Code deformation and lattice surgery are gauge fixing

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Abstract

The large-scale execution of quantum algorithms requires basic quantum operations to be implemented fault-tolerantly. The most popular technique for accomplishing this, using the devices that can be realized in the near term, uses stabilizer codes which can be embedded in a planar layout. The set of fault-tolerant operations which can be executed in these systems using unitary gates is typically very limited. This has driven the development of measurement-based schemes for performing logical operations in these codes, known as lattice surgery and code deformation. In parallel, gauge fixing has emerged as a measurement-based method for performing universal gate sets in subsystem stabilizer codes. In this work, we show that lattice surgery and code deformation can be expressed as special cases of gauge fixing, permitting a simple and rigorous test for fault-tolerance together with simple guiding principles for the implementation of these operations. We demonstrate the accuracy of this method numerically with examples based on the surface code, some of which are novel.

1. Introduction

Quantum computers can implement algorithms which are much faster than their classical counterparts, with exponential speedup for problems such as prime factorization [1], and polynomial speedup for many others [2]. The main obstacle to constructing a large-scale quantum computer is decoherence, which partially randomizes quantum states and operations. Although state-of-the-art coherence times are now appreciably longer than gate times [3, 4], they remain too short for useful quantum computation.

To counter the effect of decoherence on quantum states which are stored or manipulated imperfectly, we can encode logical qubit states into several physical qubits, and perform non-destructive multi-qubit measurements of the resulting system to extract information about which errors have occurred, called the *syndrome*. The spaces of multi-qubit states used to encode these logical states are called *quantum error-correcting codes*, and their ability to correct errors is measured by the *distance d*, which is the number of independent errors (or error *weight*) necessary to alter the state of the logical qubits without being detected. In order to use one of these codes in practice, it is also necessary to account for the effect of decoherence on operations. For example, a syndrome measurement may involve a sequence of entangling gates, and the error caused by a faulty gate on a small set of qubits in the beginning of the circuit may propagate onto many qubits, producing a high-weight error, increasing the likelihood of a logical error. Measurement results can also be corrupted by decoherence, so syndrome extraction often has to be repeated. In order to prevent error propagation during repeated measurement, syndrome extraction circuits must be designed such that a small number of faults (from imperfect gates or memory errors on data qubits) will result in a small number of errors on the physical qubits, which can be corrected using noisy syndromes. Given a family of codes of different distances, we can determine a *threshold error rate*, the rate beneath which codes with higher distance produce lower logical error probabilities.

Several such families of quantum error-correcting codes have been developed, including concatenated codes [5,6], subsystem codes such as Bacon–Shor codes [7], and 2D topological codes. The most prominent 2D topological codes are surface codes [8] derived from Kitaev's toric code [9], which we will focus on in the remainder of this manuscript. 2D topological codes can be implemented using entangling gates which are local in two dimensions, allowing fault-tolerance in near-term devices which have limited connectivity. In addition, 2D topological codes generally have high fault-tolerant memory thresholds, with the surface code having the highest at $\sim 1\%$ [10].

These advantages come at a cost, however. While other 2D topological codes permit logical single-qubit Clifford operations to be implemented *transversally*, the surface code does not. In addition, the constraint that computation be carried out in a single plane does not permit two-qubit physical gates to be carried out between physical qubits in different code blocks, precluding the two-qubit gates which, in principle, can be carried out transversally.

These two restrictions have led to the design of measurement-based protocols for performing single- and two-qubit logical gates by making gradual changes to the underlying stabilizer code. Measurement-based protocols that implement single-qubit gates are typically called *code deformation* [11], and protocols that involve multiple logical qubits are usually called *lattice surgery* [12]. A separate measurement-based technique, called *gauge fixing* [13], can be applied to *subsystem codes*, which have operators which can be added to or removed from the stabilizer group as desired, the so-called *gauge operators*. During gauge fixing, the stabilizer generators of the subsystem code remain unchanged, and can be used to detect and correct errors; so decoding is unaffected by gauge fixing. This is in contrast to code deformation and lattice surgery, where it is not *a priori* clear which measurement results to incorporate into decoding, or how to process them. Recently, many different code deformation and lattice surgery techniques have been devised, most of which use tailor-made analysis or decoding techniques, see e.g. [14–21].

In this paper, we phrase existing lattice surgery and code deformation protocols as special cases of gauge fixing, showing that the underlying subsystem code dictates the fault-tolerance properties of the protocol. This perspective can simplify the analysis of new measurement-based protocols, provided that they are based on stabilizer codes whose distances can be easily calculated. Also, knowing the stabilizer of the underlying subsystem code results in clear guidelines for decoding using the measurement results produced by such a protocol.

The remainder of this paper is organized as follows. In section 2, we review the ideas behind code deformation and lattice surgery. In section 3, we review the formalism of gauge fixing. Following this, in section 4, we formulate lattice surgery and code deformation operations as gauge fixing, demonstrating that fault-tolerant code deformation protocols are in fact based on high-distance subsystem codes. We also show this explicitly using both well-known and novel protocols. In section 5, we numerically determine the performance of these protocols. We conclude and discuss potential future research in section 6.

In all figures in this paper, qubits are located on the vertices of the drawn lattice. We refer to the local generators of the stabilizer group of the surface code as *stabilizers* or *checks*. In the figures, black regions signify *X*-stabilizers and light gray regions *Z*-stabilizers, with no stabilizers measured on white plaquettes.

2. Code deformation and lattice surgery

2.1. Code deformation

Code deformation is a technique to convert one code into another by making a series of changes to the set of stabilizer generators to be measured in each round of error correction. Typically, these protocols use ancillae prepared in entangled and/or encoded states as a resource. Also, a typical code deformation sequence proceeds gradually, first expanding the code into a large intermediate code by entangling the original code block with the ancillae, then disentangling some of the qubits (which may include some or all of the original data qubits), producing a final code which can then be used for further computation. The initial and final code may differ in their logical operators, in which case the deformation performs a logical operation. Also, the initial and final code may differ in their position or orientation within a larger quantum computer.

For example, consider the proposed fault-tolerant procedure for lattice rotation of surface codes shown in figure 1, similar to the one presented in [22]. One can see five steps which gradually modify the surface code patch starting at the bottom right of figure 1(a) and ending at the top left of figure 1(e) in a different orientation. First, three ancillary patches are prepared in fixed states, and placed near the upper left corner of the target patch. Then, the patch undergoes a two-step growing operation, followed by a two-step shrinking operation. Advancing one step is done by measuring the operators corresponding to the new stabilizers, some of which anti-commute with the old ones. Measurement of these new stabilizers will return ± 1 values at random. This means that additional corrections, unrelated to errors that may have occurred, are needed in order to enter the

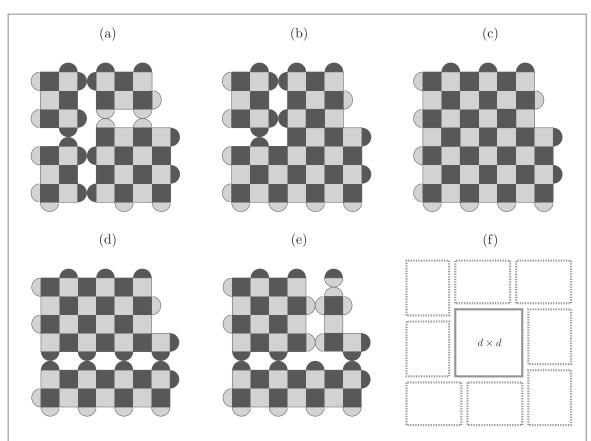


Figure 1. Fault-tolerant procedure for rotating a surface code by 90° and reflecting it about the x axis (see [12, figure 10] for the corresponding protocol using smooth/rough boundaries). (a) Initial layout where the 5×5 lattice is to be rotated, the three 3×4 patches are ancillas in fixed states, fully specified by the stabilizers shown. (b) Intermediate lattice, this step is required to expand the lattice fault-tolerantly. (c) Fully expanded lattice. (d) and (e) Splitting operations performed to shrink the lattice. (f) By using the two steps from (a) to (c) at the same time on all corners, one can grow a lattice from distance d to 3d-4. The surrounding ancillary patches have $(d-2) \times (d-1)$ qubits each.

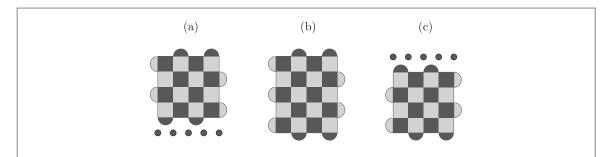


Figure 2. A procedure to flip a lattice using code deformation. (a) The lattice to be flipped, and the physical qubits prepared in $|+\rangle$ states. (b) The flip operation is realized by merging the original lattice with the physical qubits below. (c) Subsequently measuring the physical qubits at the top in the X basis finishes the flip operation.

new code space (the mutual +1-eigenspace of all new stabilizers). Moreover, to account for noisy operations, one must simultaneously perform error correction. After one is confident that the encoded state is in the new code space, one can proceed to the next step.

In section 4, we will demonstrate that, following these five steps, one can fault-tolerantly protect the logical information at all times with a distance-5 code. We also show that the distance would be reduced to 3 if one were to omit step (b), going directly from (a) to (c), as one would do when directly adapting the established surface code rotation method from [12] to rotated surface codes.

This lattice rotation followed by the lattice flip in figure 2 are useful for performing a transversal Hadamard gate. The transversal Hadamard gate on a surface code patch, performed by applying a Hadamard gate on each qubit, interchanges X and Z plaquettes. This code transformation can be undone by a lattice rotation, followed by a lattice flip. Moreover, part of this rotation procedure can be used to grow a code with distance d to a code with distance d in two steps by simultaneously growing all corners, see figure 1(f).

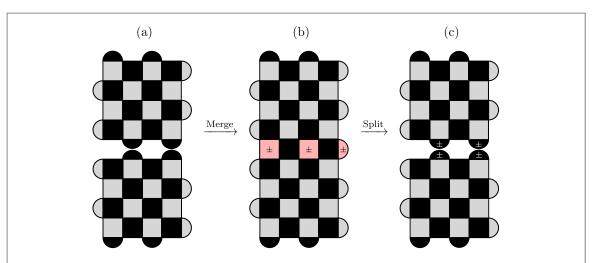


Figure 3. Lattice surgery for the rotated surface code. A ' \pm ' label indicates a random sign for the corresponding plaquette in the stabilizer group. (a) Initial layout, two rotated surface codes. (b) The merged lattice, which is a surface code with random \pm signs on the newly-measured (red) plaquettes. (c) The *split* lattices, in which the original stabilizers are measured again. Random \pm signs are produced on the boundary X-stabilizers.

This type of code deformation does not, in itself, perform logical operations, but can be used to move patches of code or to convert between codes where different gates are transversal [16]. Other code deformation procedures such as moving holes or twists do perform unitary logical Clifford operations [18, 23, 24]. In the next section, we present another similar procedure which executes a logical measurement.

2.2. Lattice surgery

Lattice surgery is a particular measurement-based procedure that acts non-trivially on the logical information. By going through two steps of deformation, it implements a joint measurement of logical operators, typically $\overline{X}_1 \overline{X}_2$ or $\overline{Z}_1 \overline{Z}_2$, where \overline{X}_j and \overline{Z}_j denote the logical operators of the logical qubit j. We will focus on the $\overline{Z}_1 \overline{Z}_2$ measurement and review the protocol used for the surface code [12, 15].

Consider two patches of $L \times L$ rotated surface code, as in figure 3(a). Each has a \overline{Z} logical operator along the boundary which faces the other patch. In the merge step, one measures the intermediary Z-plaquettes (in red in figure 3(b)). These plaquettes are such that the product of all outcomes is the outcome of the $\overline{Z_1}\overline{Z_2}$ measurement, but any subset of these outcomes produces a random result when multiplied together. This ensures that the only non-stabilizer operator whose eigenvalue can be inferred from these measurements is $\overline{Z_1}\overline{Z_2}$. These measurements do not commute with the weight-2 X stabilizers at the joint boundary (in figure 3(b)). The Gottesman-Knill theorem [25] prescribes how to update the stabilizer after such measurements, namely we only retain elements in the original stabilizer group which do commute with the newly measured stabilizers. This implies that the code becomes a $2L \times L$ patch of surface code, apart from some minus signs on the newlymeasured Z-checks. This merge step is very similar to the rotation presented before, except that some logical information is learned in the process and the additional corrections which fix the state into the new code space may involve one of the original logical operators (when the number of intermediary plaquettes with -1eigenvalues is odd). To finish the protocol, the original code space must be restored by performing a splitting operation, measuring the original stabilizers of the two separate patches instead of the intermediary Zplaquettes. Those Z-plaquettes, as in the merge step, anticommute with the boundary X-stabilizers, and will be removed from the stabilizer group. Their product, equal to $\overline{Z}_1\overline{Z}_2$, does commute, and will remain as a stabilizer of the final state. In addition, the boundary X-plaquettes will have random \pm signs which are perfectly correlated between facing pairs. Therefore, one can eliminate these \pm signs by applying some of the former stabilizers (those supported on the intermediary *Z*-plaquettes).

One can check (see the algebraic proof in appendix A) that depending on the outcome (± 1) of the logical $\overline{Z}_1\overline{Z}_2$ measurement, the merge and split operations, respectively M_{\pm} and S_{\pm} can be expressed as

$$M_{+} = |\overline{0}\rangle\langle\overline{00}| + |\overline{1}\rangle\langle\overline{11}|, \qquad S_{+} = |\overline{00}\rangle\langle\overline{0}| + |\overline{11}\rangle\langle\overline{1}|, \tag{1}$$

$$M_{-} = |\overline{0}\rangle\langle\overline{01}| + |\overline{1}\rangle\langle\overline{10}|, \qquad S_{-} = |\overline{01}\rangle\langle\overline{0}| + |\overline{10}\rangle\langle\overline{1}|. \tag{2}$$

They are related to the projections, P_+ , onto the ± 1 eigenspace of $\overline{Z_1}\overline{Z_2}$ by composition:

$$P_{+} = S_{+} \circ M_{+}, \qquad P_{-} = S_{-} \circ M_{-}.$$

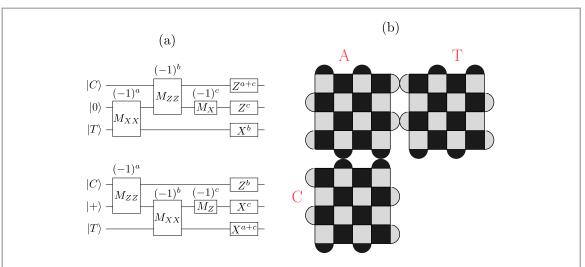


Figure 4. (a) Two equivalent measurement-based circuits for the CNOT gate. (b) The qubit layout for a CNOT gate between two surface-code qubits. C is the control qubit, T is the target qubit, and A is a logical ancilla.

In particular, lattice surgery allows us to implement the measurement-based CNOT gate [26] in a 2D layout with only local operations as shown in figure 4. We note that a more general set of operations which can be implemented by lattice surgery can be constructed using the relation between the merge and split operations considered here and the three-legged nodes of the ZX-calculus [27]. For the purposes of this work, however, we will limit our discussion to CNOT gates.

3. Gauge fixing

Gauge fixing [13] is an approach which has been used to implement universal fault-tolerant gate sets in *subsystem codes* [28]. A subsystem code is equivalent to a stabilizer code in which some of the logical qubits are not used to carry any logical information. These logical qubits are called *gauge* qubits and they can be acted on or measured without disturbing the states of the other logical qubits, which are used to store and process quantum information. Then, one way to formally define a subsystem code, C, is to define a subgroup of the Pauli group, called the *gauge group* C, containing all the Pauli stabilizers as well as the Pauli operators defining the gauge qubits. This subgroup is non-Abelian as it contains anti-commuting Pauli operator pairs which represent the gauge qubit logical operators. The stabilizer group, C, can be derived from C0 as its center, denoted C1, i.e. containing all elements in C2, which commute with all the elements of C3.

$$S = Z(G) = C(G) \cap G, \tag{3}$$

where C(G) denotes the centralizer of G in the Pauli group, i.e. all elements in the Pauli group, which commute with all elements in G. Elements in G which are not in G are the Pauli operators acting non-trivially on the gauge qubits: this is the set of non-trivial gauge operators \mathcal{L}_g

$$\mathcal{L}_{\sigma} = \mathcal{G} \backslash \mathcal{S}. \tag{4}$$

Following this, one can define operators for the actual logical qubits which by definition are elements in $\mathcal{C}(S) \setminus S$. If these operators act trivially on the gauge qubits, we call these *bare* logical operators. Bare logical operators can be multiplied by elements in \mathcal{L}_g to become *dressed* logical operators which also act on the gauge qubits. We can write

$$\mathcal{L}_{\text{bare}} = \mathcal{C}(\mathcal{G}) \setminus \mathcal{G}, \qquad \mathcal{L}_{\text{dressed}} = \mathcal{C}(\mathcal{S}) \setminus \mathcal{G}.$$
 (5)

Note that with this definition we have, $\mathcal{L}_{bare} \subset \mathcal{L}_{dressed}$. The distance of the subsystem code C is the smallest weight of any of its non-trivial logical operators,

$$d_C = \min_{\ell \in \mathcal{L}_{\text{dressed}}} \text{wt}(\ell). \tag{6}$$

One advantage of subsystem codes is that to measure stabilizers, one is free to measure any set of checks in the gauge group as long as this set generates the stabilizer group. By measuring elements in the full gauge group, one can put the gauge qubits in specific states, permitting different sets of transversal logical gates. This act of putting the gauge qubits in a specific state is called *gauge fixing*. The idea is to measure a commuting subset of gauge operators (all the Z-type gauge operators, for example), obtaining ± 1 outcomes and applying the anticommuting, or *conjugate* partner operator (an X-type gauge operator in the example), wherever a -1

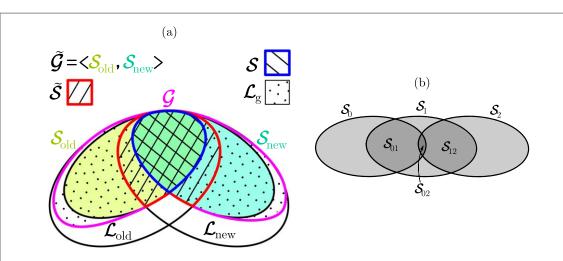


Figure 5. Venn diagrams depicting the relations between the different sets of Pauli operators concerning the gauge group $\mathcal G$ of interest, see main text. (a) For one step, the yellow set represents the old stabilizer group, $\mathcal S_{\rm old}$, and the blue set the new group, $\mathcal S_{\rm new}$. Both are surrounded by the logical operators, $\mathcal L_{\rm old}$ and $\mathcal L_{\rm new}$ respectively. The gauge group generated by both, $\tilde{\mathcal G} = \langle \mathcal S_{\rm old}, \mathcal S_{\rm new} \rangle$, has $\tilde{\mathcal S}$ as its center, shown by the down-left-dashed region outlined in red. The gauge group of interest, $\mathcal G$, is outlined in purple and has $\mathcal S$, in the down-right-dashed region outlined in blue as its center. The set of gauge operators defining the gauge qubits, $\mathcal L_{\mathcal G}$, is the dotted region. When switching from $\mathcal S_{\rm old}$ to $\mathcal S_{\rm new}$ one fixes the gauge for the elements in the blue dotted region $\mathcal M_{\rm fix} = \tilde{\mathcal G} \setminus \mathcal S_{\rm old}$. (b) One possible scenario for two successive steps of deformation. Doing it in two steps, i.e. from $\mathcal S_0 \to \mathcal S_1$, and then from $\mathcal S_1 \to \mathcal S_2$ permits to use successively the stabilizer groups $\mathcal S_{01}$ and then $\mathcal S_{12}$ for error correction. Skipping the intermediary steps, one can only use $\mathcal S_{02}$ which might offer less protection.

outcome has been obtained. In the example, this would fix all gauge qubits to the $|0\rangle$ state. While the gauge is fixed in this way, the Z-type gauge operators become elements of the stabilizer group, so S is augmented to some larger Abelian subgroup of G. Appendix B shows an example of how code conversion between the [7, 1, 3] Steane code to the [15, 7, 3] Reed–Muller code can be viewed as gauge fixing.

4. Fault-tolerance analysis with gauge fixing

In this section, we show how both code deformation and lattice surgery can be viewed as gauge fixing operations and therefore, one can use gauge fixing to analyze the fault-tolerance of these operations.

We consider the quantum error-correcting codes before and after a deformation step, denoted as C_{old} and C_{new} , with stabilizer groups S_{old} and S_{new} , respectively. Both codes are fully defined on the same set of qubits. The non-trivial logical operators of each code are defined as

$$\mathcal{L}_{\mathrm{old}} = \mathcal{C}(\mathcal{S}_{\mathrm{old}}) \setminus \mathcal{S}_{\mathrm{old}}, \qquad \mathcal{L}_{\mathrm{new}} = \mathcal{C}(\mathcal{S}_{\mathrm{new}}) \setminus \mathcal{S}_{\mathrm{new}}.$$

The intuition we follow is to see the two stabilizer codes as two different gauges of the same subsystem code. The first step, then, is to define a joint subsystem code, \tilde{C} , whose gauge group, $\tilde{\mathcal{G}}$, is generated by both \mathcal{S}_{old} and \mathcal{S}_{new} ,

$$\tilde{\mathcal{G}} = \langle \mathcal{S}_{\text{old}}, \, \mathcal{S}_{\text{new}} \rangle.$$

The generated group, $\tilde{\mathcal{G}}$, is not necessarily Abelian, since it contains elements of \mathcal{S}_{old} which may anti-commute with some elements of \mathcal{S}_{new} .

The stabilizer group, \tilde{S} , defined as in equation (3), can be characterized as follows: Elements in the center of \tilde{G} also have to be in the centralizers of S_{old} and S_{new} . Moreover, being in both centralizers and in \tilde{G} is sufficient to be in the center, or

$$\tilde{\mathcal{S}} = \mathcal{C}(\mathcal{S}_{\text{old}}) \cap \mathcal{C}(\mathcal{S}_{\text{new}}) \cap \tilde{\mathcal{G}}.$$

See figure 5(a) for a representation of $\tilde{\mathcal{S}}$ as a Venn diagram. Note that, in addition to containing $\mathcal{S}_{\text{old}} \cap \mathcal{S}_{\text{new}}$, $\tilde{\mathcal{S}}$ can also contain some logical operators from either \mathcal{L}_{old} or \mathcal{L}_{new} . This is the case for the merge operation of lattice surgery where the logical $\overline{Z_1}\overline{Z_2} \in \mathcal{L}_{\text{old}}$ but also $\overline{Z_1}\overline{Z_2} \in \mathcal{S}_{\text{new}}$ and therefore $\overline{Z_1}\overline{Z_2} \in \tilde{\mathcal{S}}$. Similarly, for the split operation $\overline{Z_1}\overline{Z_2} \in \mathcal{L}_{\text{new}}$ but also in \mathcal{S}_{old} and therefore in $\tilde{\mathcal{S}}$.

As defined above, this subsystem code \tilde{C} indeed admits \mathcal{S}_{old} and \mathcal{S}_{new} as two distinct Abelian subgroups of $\tilde{\mathcal{G}}$. Therefore the codes \mathcal{S}_{old} and \mathcal{S}_{new} correspond to fixing two different sets of states for the gauge qubits of $\tilde{\mathcal{G}}$. However, for this to function as a subsystem code, one would have to be stabilized at all times by $\tilde{\mathcal{S}}$ and thus be able to measure all values of the stabilizers of $\tilde{\mathcal{S}}$.

This is not the necessarily the case when \tilde{S} contains some elements of \mathcal{L}_{old} or \mathcal{L}_{new} , and we have to further modify $\tilde{\mathcal{G}}$ to a gauge group \mathcal{G} whose center is solely

$$Z(\mathcal{G}) = \mathcal{S} = \mathcal{S}_{old} \cap \mathcal{S}_{new}$$
.

How do we obtain $\mathcal G$ from $\widetilde{\mathcal G}$? This new gauge group, $\mathcal G$ will be generated by $\mathcal S_{\text{old}}$ and $\mathcal S_{\text{new}}$ in addition to (anti-commuting) conjugate partners of elements in the sets $\mathcal M_{\text{prep}} = \mathcal S_{\text{old}} \cap \mathcal L_{\text{new}}$ and $\mathcal M_{\text{meas}} = \mathcal S_{\text{new}} \cap \mathcal L_{\text{old}}$. More precisely, one views $\mathcal M_{\text{prep}}$ as a subset of $\mathcal L_{\text{new}}$, and for each independent logical operator contained in $\mathcal M_{\text{prep}}$ adds a chosen conjugated partner within $\mathcal L_{\text{new}}$. One operates similarly for $\mathcal M_{\text{meas}}$ by viewing it as a subset of $\mathcal L_{\text{old}}$. If we then consider the center of $\mathcal G$, we see that all elements in $\mathcal M_{\text{prep}}$ and $\mathcal M_{\text{meas}}$ are excluded from it since they anti-commute with some elements in $\mathcal G$. This means that the center of $\mathcal G$ is reduced to $\mathcal Z(\mathcal G) = \mathcal S_{\text{old}} \cap \mathcal S_{\text{new}}$ as desired.

The names \mathcal{M}_{prep} and \mathcal{M}_{meas} are chosen to represent their respective roles in the deformation procedure. In such a procedure one starts from a system encoded in C_{old} , i.e. stabilized by S_{old} , and then one measures the new stabilizers, S_{new} . When S_{new} contains some elements of L_{old} , then in general these elements will not stabilize the state of the system, since it can be in any logical state at the beginning of the procedure. Measuring these operators will return information about the logical state and cannot return information about errors. Thus, by switching to S_{new} one also performs a logical measurement of the elements in \mathcal{M}_{meas} .

It is also possible for S_{old} to contain some elements of \mathcal{L}_{new} . In that case, the state of the system is initially stabilized by these elements, and remains so, since we only measure operators commuting with them. In this sense, the deformation procedure will prepare the logical +1 state of elements in \mathcal{M}_{prep} .

We denote the code underlying the code deformation step as C. Its gauge group, \mathcal{G} , is represented as a Venn diagram in figure 5(a). Thus the deformation operation that transforms C_{old} into C_{new} is realized by switching what gauge to fix of the code C: in one gauge one obtains C_{old} , the other gauge gives C_{new} . Since the deformation step can also transform logical information, what gauge elements are fixed is subtle. Namely, note that in this gauge fixing of C to either code C_{old} or C_{new} the gauge elements in $\mathcal{G}\setminus\tilde{\mathcal{G}}$ will never be fixed. Said differently, only the elements of \mathcal{L}_g which are in the blue dotted region in figure 5 will be fixed, one can also view these as elements of $\mathcal{M}_{\text{fix}} \equiv \tilde{\mathcal{G}}\setminus\mathcal{S}_{\text{old}}$.

4.1. Fault-tolerance of code deformation

Given an underlying subsystem deformation code *C*, one can ensure the fault-tolerance of a code deformation operation by checking three criteria:

- 1. *Code distance*: The distance of the subsystem code, C, must be large enough for the desired protection. Ideally it matches the distances of $C_{\rm old}$ and $C_{\rm new}$ so the degree of protection is not reduced during the deformation step.
- 2. *Error correction:* The error correction procedure follows that of the subsystem code *C* through the code deformation step.
- 3. *Gauge fixing*: To fix the gauge, one has to use operators exclusively from $\mathcal{L}_g = \mathcal{G} \setminus \mathcal{S}$.

More specifically, criterion 2 means that to perform error correction, one has to reconstruct from the measurements of S_{new} the syndrome given by S. Importantly, criteria 2 and 3 demonstrate that the processes of error correction and that of gauge fixing are two separate processes with different functionality. Both processes require the application of Pauli operators (in hardware or in software) to make sure that stabilizer measurements are corrected to have outcome +1. The error correction process does this to correct for errors, while the gauge-fixing process does this to move from C_{old} to C_{new} .

This description holds for one step of deformation, so that for each step in a sequence of deformations one has to examine the corresponding subsystem code C and its distance. Depending on the sequence, figure 5(b) illustrates why skipping steps could lead to poor distance and poor protection against errors. This discussion also assumes that stabilizer measurements are perfect; the effect of noisy stabilizer measurements is considered in the following section.

4.1.1. Noisy measurements

When one considers noisy syndrome measurements, one needs to ensure that both the stabilizer outcomes and the state of the gauge qubits can be learned reliably. For 2D stabilizer codes such as the surface code this is simply done by repeating the measurements. To process this repeated measurement information for the surface code, one no longer uses the syndrome but the *difference syndrome*: the difference syndrome is marked as non-trivial (we say that a *defect* is present) only when the syndrome value changes from the previous round of measurement. This difference syndrome or defect gives information about both qubit errors as well as measurement errors.

How do we construct the difference syndrome at the code deformation step T_d in figure 6? At T_d one obtains the syndrome for the code S_{new} . For those elements in S_{new} which are in S, we know what this syndrome should have been if no measurement or data errors had occurred since the previous QEC round which measured the

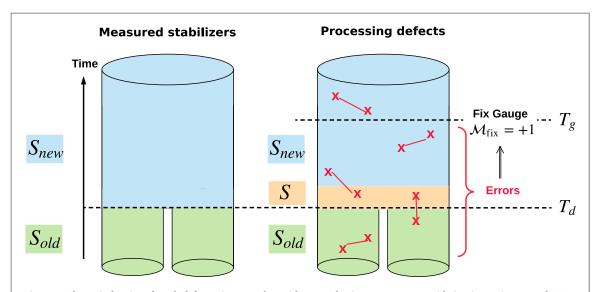


Figure 6. Schematic drawing of a code deformation procedure with repeated noisy measurements, with time increasing upwards. T_d designates the time step at which the code deformation (the switch from measuring the checks of S_{old} to those of S_{new}) is performed. T_g is the time at which one is confident enough about the state of the gauge qubits, taking into account errors, to fix their states. This means that, after T_g , another logical computation can be performed. (Right) The first round of measurement of S_{new} at time T_d does not have a previous value to compare to in order to construct a difference syndrome, i.e. one can only construct defects for S. Immediately after this step, one can derive the difference syndrome of the full S_{new} , placing defects accordingly. Using defects before and after T_d , one processes error information to infer the value of the gauge operators in \mathcal{M}_{fix} at time T_g , thus fixing the gauge at T_g .

stabilizers of S_{old} . Therefore, we can place defects when the found syndrome changes from what it was expected to be based on the last round of measurements with S_{old} . S_{new} also contains a subset of elements in L_g , namely the blue dotted region \mathcal{M}_{fix} in figure 5(a). Some of these elements are also contained in \mathcal{L}_{old} (down-right-dashed area in figure 5(a)), i.e. they are elements of $\mathcal{M}_{\text{meas}}$. The eigenvalues of these elements in $\mathcal{M}_{\text{meas}}$ depends on the logical state and are therefore not a proper syndrome for S_{old} . So only after one more round of syndrome extraction with S_{new} one can mark whether the syndrome for these elements in $\mathcal{M}_{\text{meas}}$ changes, and either place a defect or not. In addition, the eigenvalues of the gauge operators in the remaining blue dotted region take random ± 1 eigenvalues (since they anticommute with some elements in S_{old}): for these checks, like for the elements in $\mathcal{M}_{\text{meas}}$, there is no previous record to construct a difference syndrome right away. Again, only after one round of syndrome extraction with S_{new} one can again mark whether the syndrome changed, placing a defect for an element or not. In processing these new syndromes of S_{new} to do error correction, we should also allow them to be matched with virtual defects placed beyond the past-time boundary T_d . For example, a measurement error in the first step when the syndrome is randomly +1 or -1, followed by many rounds without measurement error, produces a single defect and should be interpreted as the first measurement being incorrect. In this sense, there is only one layer of time where the defects are those of S as indicated on the right in figure 6.

Given all defect syndromes, minimum-weight matching can be used to decode (see figure 6), to infer some errors as they have occurred in a window of time before and after T_g and T_d (one may use a sliding window as in [22]). Let us then imagine that by matching defects in a window which goes beyond a so-called gauge-fixing time T_g , one infers a set of measurement and data errors. These errors are projected forwards to the time-slice T_g and they are used to do three things. One is to correct the value of elements in M_{meas} (if any), so that the logical measurement has been completed and properly interpreted. The second is to determine or fix the gauge, i.e. determine the outcome of elements \mathcal{M}_{fix} in the blue dotted region of figure 5. As we have argued, these gauge values may be ± 1 at random and hence Pauli gauge-fixing corrections can be added in software to make the outcomes all +1 if one wishes to work with the frame where all elements in \mathcal{S}_{new} have +1 eigenvalue. These Pauli gauge-fixing corrections are not error corrections and any set of Pauli operators can be chosen as long as they solely fix the values of the elements in \mathcal{M}_{fix} . Thirdly, the projected errors provide the usual update of the Pauli frame for the code \mathcal{S} , so together with the gauge-fixing corrections, for the code \mathcal{S}_{new} . The whole procedure is represented schematically in Figure 6; at time T_g , the code deformation step is finished.

Note that, after T_d , the elements in \mathcal{M}_{prep} are no longer measured, but their fixed values before the code deformation now represent logical states prepared by code deformation. Typically, for 2D stabilizer codes, the time window between T_g and T_d needs be of size O(d) in order to fix the gauge, where d is the distance of code C. In some cases, the measurements contain enough redundant information about the gauge operators so that T_g can be equal to T_d (e.g. in single-shot error correction schemes based on redundancy of the checks). For example, this is the case when performing the logical measurement of a patch of code by measuring every single qubit in the Z basis. This is also the case for the logical measurement step of the plain surgery technique explained below.

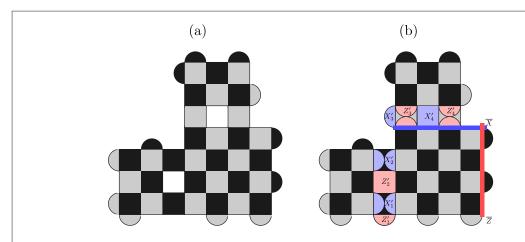


Figure 7. Description of the subsystem code, C, which holds during the first step of the grow operation depicted in figures 1(a) and (b). (a) Generators for the stabilizer group, S, of C. (b) Generators for the whole gauge group G of G. Highlighted in red and blue, respectively, are gauge operators, elements of \mathcal{L}_g , of G-type and G-type, respectively. The logical operators, G-type are also represented in brighter colors.

In the remainder of this section, we apply this formalism to the code deformation and lattice surgery operations discussed earlier.

4.2. Code deformation examples

4.2.1. Grow operations

Gauge fixing, when applied to the growing operations of figures 1 and 2, reveals an underlying subsystem code with a small number of widely-spaced holes and large boundaries, resulting in a high distance. The stabilizer group, S, as well as the gauge operators, \mathcal{L}_g , for the subsystem code C which governs the deformation from figures 1(a) to (b), are shown in figure 7.

In all figures of this paper, light blue and light red patches individually represent X-type and Z-type gauge operators, and bright blue and bright red qubit chains are \overline{X} and \overline{Z} operators respectively. The grow operation is changing the gauge from one in which the gauge operators not overlapping between the initially separate patches are fixed, denoted as $\{X'_1, X'_2, X'_3, X'_4\}$ in figure 7(b), to one in which the overlapping ones are fixed, denoted as $\{Z'_1, Z'_2, X'_3, X'_4\}$ in figure 7(b). The distance of C is still 5, matching the distance of the initial code.

Now consider what happens if we would go directly from figures 1(a) to (c). The stabilizers and the gauge operators for this operation are shown in figure 8. Similarly, one fixes the gauge going from separate patches to a single patch. The distance of the subsystem code for this operation is only 3. Indeed one of the minimum-weight dressed logical operators is the \overline{Z} on the qubits in the green box in figure 8(b). That means that, in order to preserve the code distance, one should perform the intermediary step.

4.2.2. The merging and splitting operations

In this section, we interpret the joint measurement of $\overline{Z}\overline{Z}$ by lattice surgery in figure 3(b) as gauge fixing. The stabilizer group S is generated by all the stabilizers in figure 9(a). The gauge operators, \mathcal{L}_g , of the gauge group are given by three representatives of the logical X of the top patch and the intermediary Z plaquettes that anticommute with them. They are denoted as $(X'_1, Z'_1, X'_2, Z'_2, X'_3, Z'_3)$ in figure 9(b). Representatives of the bare logical operators, \overline{X} , $\overline{Z} \in \mathcal{L}_{bare}$, are the logical Z of the bottom patch and the logical X of the merged patch (joining the very top to the very bottom), see figure 9(b). The merge and split operations are realized by fixing some gauge operators of \mathcal{L}_g , resulting in new codes C_{merged} or C_{split} , respectively. Note that the weight of \overline{X} of the subsystem code, C, is only d and not 2d which is the distance for X of the merged code. Indeed, by using the gauge operators like X_1' and stabilizers, one can construct a dressed logical X of weight d. Another way of seeing this is by realizing that one cannot distinguish between two errors of weight $\frac{d}{d}$ depicted in figures 9(c) and (d). In the first one, the logical measurement outcome is -1 and there is a string of $\frac{d}{2}$ X-errors from the bottom to the middle of the bottom patch. In the second one the logical measurement outcome is +1 and there is a string of $\frac{d}{dt}$ X-errors from the middle of the bottom patch and the middle (changing the observed logical measurement outcome to -1). Note also that when performing the splitting operation, one wants to correct the -1 outcomes for some of the intermediary X stabilizers. They are gauge operators equivalent to, say $X_1'X_2'$. They have to be corrected using the Z gauge operators, say Z'_1 in this case. Otherwise one would introduce a logical Z error.

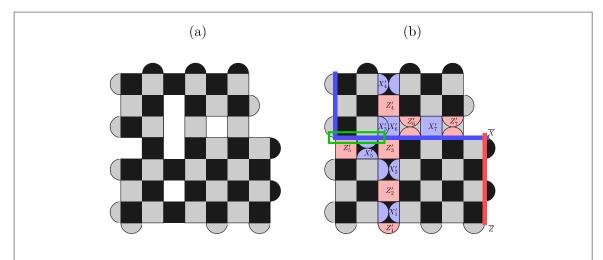


Figure 8. The operators of the subsystem code for the one-step grow operation from figures 1(a) to (c), skipping figure 1(b): (a) the stabilizers which generate S and (b) the whole gauge group, G, with highlighted gauge operators and logical operators.

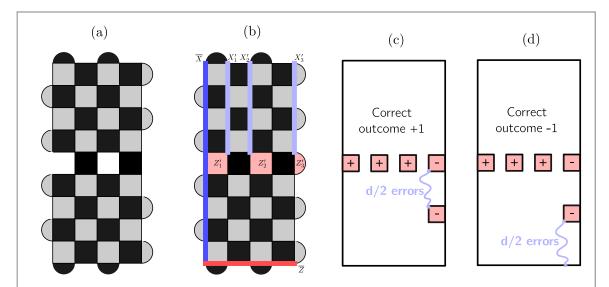


Figure 9. The operators of the subsystem code, C, for the joint measurement \overline{ZZ} . (a) The generators of stabilizer group S. (b) The highlighted operators are either gauge operators in \mathcal{L}_g or logical operators in \mathcal{L}_{bare} . We start in the gauge where the products $X_1'X_2'$ and $X_2'X_3'$ are fixed, and end in the gauge where Z_1' , Z_2' , and Z_3' are fixed. The distance of the subsystem code is 5, since one can construct a logical \overline{X} with this weight by multiplying it with X gauge operators. (c) and (d) Two different scenarios with errors of weight $\frac{d}{2}$ with the same observed measurements.

4.2.3. Plain surgery

We now introduce a new technique with the same goal as lattice surgery, namely performing joint measurements of logical operators, but following a different procedure. The difference between lattice surgery and the new procedure, *plain surgery*, will be that the logical measurement is performed with redundancy, so that this part of the protocol can be made more robust to noise, at the cost of qubit overhead.

The idea is to separate the merging and logical measurement of lattice surgery into two distinct steps. The first step deforms the two separated blocks into a single code block where the joint logical operators can be measured redundantly. Since this step merges the codes, but leaves the logical information unchanged, we call it a *plain* merge. In the second step, we measure the desired logical operator destructively, similar to the standard logical measurement of a surface code block. A final deformation step can be used to return to the original code space.

The layout for the plain merge operation is shown in figure 10(a). The patches are placed with an overlap of approximately $\frac{2d}{3}$, the *X*-boundary of one facing the *Z*-boundary of the other. Then they are merged into a single patch with 3 *X*-boundaries and 3 *Z*-boundaries, so two logical qubits. Logical operators far away from the interface are left unchanged, and the logical information is untouched. When looking at the subsystem code for this deformation, shown in figure 10(d), one can see that the distance is guaranteed by the offset between the two patches.

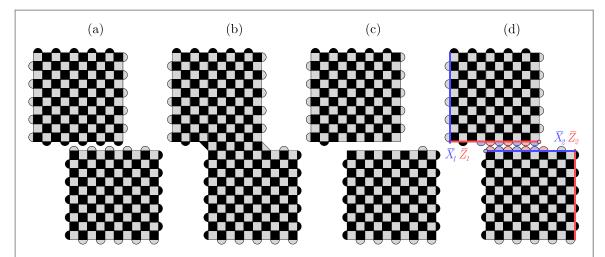


Figure 10. (a) and (b) The qubit layouts before and after the plain merge operation. The number of logical qubits is kept constant during this merge operation. (c) The stabilizers of the subsystem code. (d) The gauge operators and logical operators of the subsystem code. One can see that the distance is guaranteed by the offset between the two blocks. The distance of the separate surface codes is 11, and the distance of the subsystem code is 4.

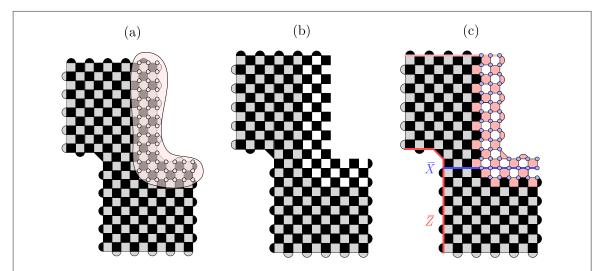


Figure 11. (a) The layout where the qubits in the region highlighted are each to be measured in the X basis. (b) The stabilizers of the underlying subsystem code C. (c) The gauge operators (in red) and logical operators of the code. One can see that the distance is guaranteed by the amount of overlap between the two blocks. The distance of the subsystem code is 4.

Then, in this new code, the logical operator $\overline{X_1}\overline{X_2}$ is given by a string starting from the top boundary of the top patch and ending on the right boundary of the bottom patch. So, by measuring qubits in the X basis in a region away from the third X-boundary, one can learn $\overline{X_1}\overline{X_2}$ but not $\overline{X_1}$ or $\overline{X_2}$. This measurement procedure is depicted in figure 11. One can check that the associated subsystem code has a distance of at least half the overlap between the patches, $\sim \frac{d}{3}$. The amount of redundancy in the measurement is also $\sim \frac{d}{3}$, which makes this procedure costly in qubit overhead but as we show in the next section, it appears to offer a better threshold than the standard lattice surgery technique.

5. Numerics

To numerically evaluate the fault-tolerance of quantum computation on rotated planar surface codes, we simulate logical measurement, rotation, logical CNOT, and plain surgery, using the Gottesman–Knill formalism [25]. These simulations are carried out using two different error models, the *phenomenological* model and the *circuit-based* model. The phenomenological error model inserts independent X and Z errors on data qubits with equal probability p, and measurements output the wrong classical value with probability p. The circuit error model inserts errors with probability p after each operation of the error correction circuit as follows: each single-qubit gate is followed by a X, Y, or Z with probability $\frac{p}{3}$, each two-qubit gate is followed by an element of $\{I, X, Y, Z\}^{\bigotimes 2} \setminus \{II\}$ with probability $\frac{p}{15}$, and each measurement returns the wrong result with probability p. In this work, except when stated otherwise, the initial logical qubits are prepared without errors when simulating these logical operations.

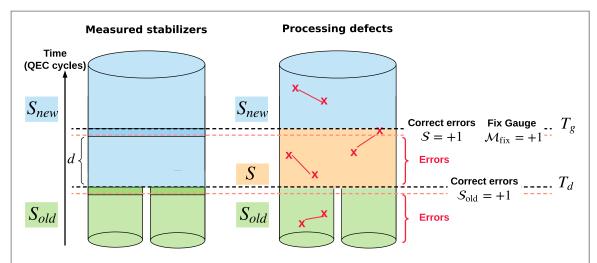


Figure 12. The simulated version of a code deformation procedure in figure 6. A perfect round (a small time window from red to black dashed lines) is inserted after each block of noisy d rounds of stabilizer measurements. One processes the defects for S_{old} and corrects errors before the code deformation step T_d . Then the defects for S are constructed at time T_d to time T_g and the 'defects' for M_{fix} are constructed one round of measurement later. At time T_g , one processes error information to infer the value of the gauge operators and then fixes the gauge.

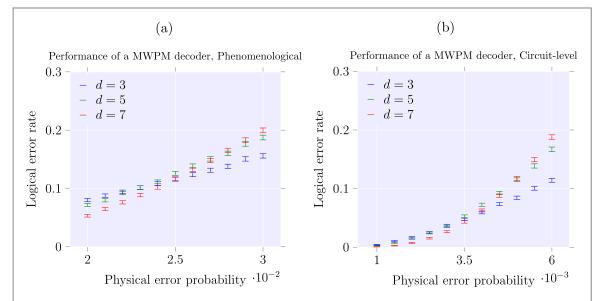
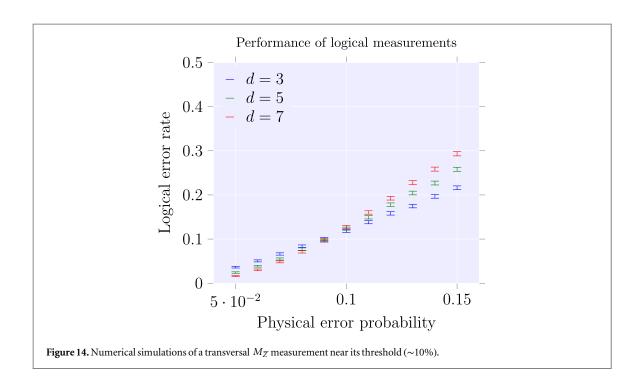


Figure 13. Numerical simulations of a fault-tolerant memory operation with the phenomenological error model near its threshold (\sim 2.75% (a)) and the circuit-level error model near its threshold (\sim 0.5% (b)).

In section 4.1, we have introduced how to construct defects (difference syndromes) for a code deformation step and how to process these defects to infer errors and fix gauge operators (figure 6). For a realistic implementation of logical operations, a decoder will infer errors in a time window which may include T_d or T_g , by processing the defects within the window. This means the decoder should be able to match defects across time boundaries, e.g. the defects before and after code deformation time T_d . In addition, it needs to construct matching graphs with edges whose endpoints are on different lattices, e.g. defects of S_{new} may be matched to virtual defects beyond the past-time boundary T_d . However, such a decoder is difficult to implement. In our simulations, we insert perfect measurement rounds after blocks of d rounds of measurement (figure 12) for ease of implementation, where d is the distance of the underlying subsystem code. A decoder using the minimum-weight perfect matching algorithm is used and its performance for a fault-tolerant memory operation, that is, d noisy quantum error correction cycles followed by 1 noiseless cycle, is shown in figure 13. For each operation (except for plain surgery), $10^5 (10^4)$ iterations were run per point and confidence intervals at 99.9% are plotted in the figures.

Single-qubit operations: Transversal operations (preparation, Pauli gates, measurement) are usually realized by performing qubit-wise physical operations. They are intrinsically fault-tolerant and their logical error rates will be only slightly higher than a logical identity gate (memory). Notably, a transversal $M_{\overline{Z}}$ ($M_{\overline{X}}$) measurement does not require several quantum error correction cycles (i.e. $T_d = T_g$) since error syndromes of Z(X)-stabilizers



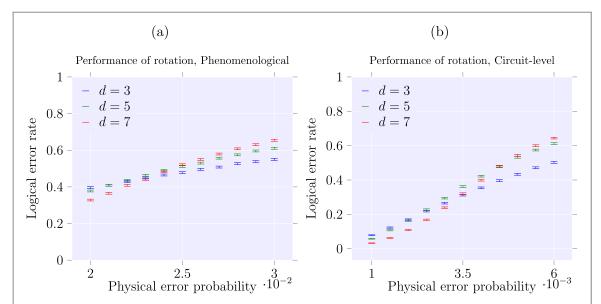


Figure 15. Numerical simulations of the rotation procedure in figure 1 without a final flip operation. (a) and (b) The logical error rates of the rotation procedure with phenomenological error model (the error threshold is around \sim 2.5%) and circuit error model (the error threshold is around \sim 0.45%), respectively.

can be reconstructed from the measurement outcomes of data qubits, this is also the case for the logical measurement step of plain surgery. For instance, one can measure all the data qubits in the Z basis to realize a $M_{\overline{Z}}$ on a planar surface code. Afterwards, one can compute the Z-syndromes by multiplying the outcomes of corresponding data qubits of each Z-stabilizer and then correct the X errors and deduce the value of \overline{Z} . The performance of a $M_{\overline{Z}}$ measurement for planar surface codes is shown in figure 14. In this simulation, we first prepare a logical qubit in state $|\overline{0}\rangle$ without errors and then perform a $M_{\overline{Z}}$ measurement on it with physical measurement error probability p. We further numerically simulate the proposed rotating procedure (figure 1) and show the results in figure 15. For the phenomenological error model, the error threshold of a rotation is slightly lower than the threshold of quantum memory. For the circuit-level error model, its threshold is similar to that of quantum memory.

Two-qubit operations: We also simulate the measurement-based CNOT circuits in figure 4(a) where the split operations of the first joint measurements are parallelized with the merge operations of the second joint measurements (see the decomposed circuits in appendix C). The overall error rates and the error thresholds for a CNOT gate by lattice surgery are shown in figure 16. For each error model, the error threshold of CNOT gates is similar to the threshold of quantum memory. Moreover, logical errors propagate through the measurement-based

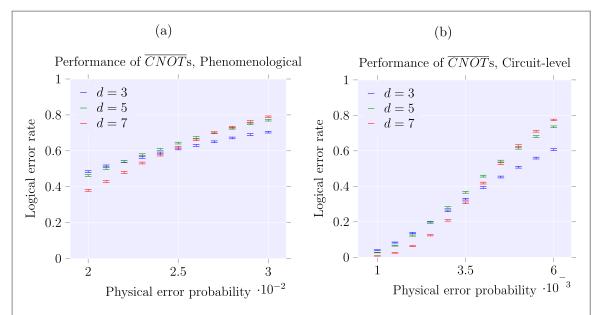


Figure 16. Numerical simulations of a measurement-based CNOT gate by lattice surgery (the top circuit in figure 4(a)). (a) Total error rates for CNOT gates with the phenomenological error model near the threshold (\sim 2.7%). (b) Total error rates for CNOT gates with the circuit-level error model near the threshold (\sim 0.45%). The fact that the crossing points corresponding to plain surgery are higher than those for lattice surgery and memory operations is curious. While this may be an intrinsic property of plain surgery, it is also possible that this data is explained by finite-size effects, discrepancy between threshold error rates when comparing even- and odd-sized lattices, or other confounding factors. While these numerical experiments confirm the validity of gauge fixing as an analytical tool in fault-tolerant quantum computing, further research is needed to analyse plain surgery in detail.

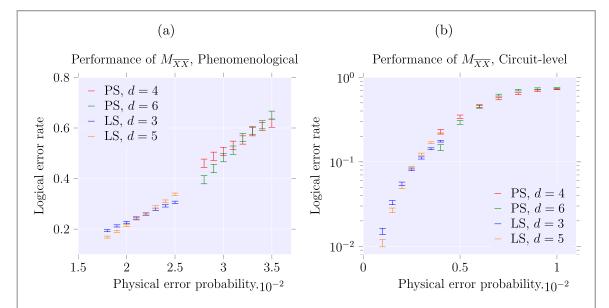


Figure 17. Numerical comparison of the $M_{\overline{XX}}$ joint measurements by lattice surgery (LS) and plain surgery (PS), near the points where the two lowest-distance implementations of the two protocols produce the same logical error rate. The logical error rates of $M_{\overline{XX}}$ with the (a) phenomenological error model ((b) circuit-level error model) by LS with a crossing between the d=3 and d=5 near the physical error probability \sim 2.2% (\sim 0.25%) and by PS with a crossing between the d=4 and d=6 near the physical error probability \sim 3.2% (\sim 0.65%).

CNOT circuits, leading to a disparity of logical error rates on control and target qubits, which is demonstrated numerically in appendix C. In addition, we compare the joint $M_{\overline{XX}}$ measurement using lattice surgery with the measurement using plain surgery. Figure 17 shows that plain surgery achieves a higher error threshold than lattice surgery, but with higher logical error rates as a consequence of the increased lattice size required to achieve a given code distance.

6. Discussion and conclusion

We have illustrated how to describe current measurement-based operations in 2D topological quantum computing using the gauge fixing technique. We have shown that, by using the formalism of gauge fixing, the fault tolerance

analysis of these code deformation and lattice surgery protocols is considerably simplified, their error correction and gauge fixing schemes also become clear. Furthermore, we numerically examined this method with examples on planar surface codes, including some well-known operations such as lattice-surgery-based CNOT gates and some novel protocols such as lattice rotation and plain surgery. Although this gauge fixing formalism does not provide direct guidlines on how to design code deformation protocols for a desired logical operation, it does provide an easy way to check the fault-tolerance of protocols and search for new ones via iterations of trial and error.

Moreover, this formalism applies not only to 2D topological codes, but more generally to any stabilizer code. In the general case (non-topological codes), the analysis of fault-tolerance in the presence of measurement errors becomes more involved, in particular with respect to how much repetition is really needed, see for example [29, 30]. We leave for future work how to obtain general and simple criteria for fault-tolerance.

Acknowledgments

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Appendix A. Algebraic proof of the correctness of the merge and split operations

In this appendix, we denote the set of physical qubits as Q. For any subset of k qubits, $s = \{j_1, ..., j_k\} \subset Q$, we denote the operator composed of a Pauli Z resp. X on each qubit in s as Z(s), resp. X(s), i.e.

$$Z(s) = Z_{j_1} \otimes \cdots \otimes Z_{j_k}, \qquad X(s) = X_{j_1} \otimes \cdots \otimes X_{j_k}.$$

A.1. Merge operation

The setting for the merge operation is drawn in figure 3(a). The starting code, $C_{\rm split}$, with stabilizer $S_{\rm split}$, consists of two adjacent $L \times L$ patches of rotated surface code with the opposite boundaries being supports for their \overline{Z} operators. We label the upper logical qubit as 1 and the lower qubit as 2. The new code, $C_{\rm merged}$, with stabilizer $S_{\rm merged}$, consists of only one $2L \times L$ patch of rotated surface code.

We define the subsystem code, C, and its gauge group, C, as specified in section 4, see figure 9. Notably, we exclude from the center of C the logical operator $\overline{Z_1}\overline{Z_2} \in S_{\text{merged}}$. We therefore add $\overline{X_1}$ to C to form C, and so have $\overline{X_1} \in C_g$. Call C the set of intermediary plaquettes (red plaquettes in figure 3(a)) to be measured to perform the merge operation. For C we have C (C) these are the gauge operators to be fixed by the merge operation. For each C0, one measures the operator C1, and let its outcome be C2.

To explain the action of the merge operation at the logical level, we first prove that this operation transforms code states of the two original $L \times L$ patches of surface code into code states of the $2L \times L$ patch surface code with some X errors. To accomplish this, we use the standard prescription from the Gottesman–Knill theorem [25]. It is straightforward to see that the original Z checks stay unchanged, and the newly-measured checks, the $p \in \mathcal{I}$, are added, with sign m_p . The original X checks all commute with the new intermediary Z checks except for the two-body boundary checks between the two patches, which are also part of \mathcal{L}_g . Those boundary checks can be merged in pairs in order to commute with the new Z checks. The situation is then the same as depicted in figure 3(b).

The product of all measurement outcomes gives the desired outcome for the $\overline{Z}_1\overline{Z}_2$ measurement, we denote it as

$$m_L = \prod_{p \in \mathcal{I}} m_p.$$

Then one fixes the gauge by applying the conjugate X-gauge operators to the Z(p) with $m_p = -1$. Let us call c_{m_L} the set of qubits involved in this fixing operation. Note that when $m_L = +1$ then the correction is equivalent to a stabilizer in S_{split} whereas when $m_L = -1$, the correction is equivalent to \overline{X}_1 . Then, the full merge operation at the physical qubit level is easily written as

$$X(c_{m_L}) \cdot \left(\prod_{p \in \mathcal{I}} \frac{\mathbb{1} + (-1)^{m_p} Z(p)}{2}\right) = \left(\prod_{p \in \mathcal{I}} \frac{\mathbb{1} + Z(p)}{2}\right) \cdot X(c_{m_L}).$$

Due to the definition of $X(c_{m_L})$, commuting it through the Z projections eliminates the $(-1)^{m_p}$ terms.

To determine the logical operation realized by this procedure, we use encoding isometries of C_{split} and C_{merged} , called E_{split} and E_{merged} , respectively. These isometries map unencoded logical states to code states in the

full physical Hilbert space. Since C_{split} contains two logical qubits and C_{merged} contains only one, the isometries have the following signatures:

$$E_{\text{split}}$$
: $\mathbb{C}^2 \otimes \mathbb{C}^2 \to \mathbb{C}^{2Q}$, E_{merged} : $\mathbb{C}^2 \to \mathbb{C}^{2Q}$.

Let \tilde{M}_{m_L} be the operation on the logical level, which can be expressed as

$$\tilde{M}_{m_L}: \mathbb{C}^2 \otimes \mathbb{C}^2 \to \mathbb{C}^2$$
,

$$\tilde{M}_{m_L} = (E_{\text{merged}})^{\dagger} \cdot \left(\prod_{p \in \mathcal{I}} \frac{1 + Z(p)}{2} \right) \cdot X(c_{m_L}) \cdot E_{\text{split}}.$$
(A1)

An important fact about encoding isometries E is that, if S is a stabilizer of the code and \overline{L} a representative for the logical operator L, then

$$S \cdot E = E, \tag{A2}$$

$$\overline{L} \cdot E = E \cdot L,\tag{A3}$$

where L is the corresponding physical operator. This means that \tilde{M}_{m_l} , defined in equation (A1), simplifies to

$$\tilde{M}_{m_L} = (E_{\text{merged}})^{\dagger} \cdot E_{\text{split}} \cdot X_1^{(1-m_L)/2}. \tag{A4}$$

To show this, we use the fact that for all $p \in \mathcal{I}$, Z(p) is a stabilizer of C_{merged} and the correction $X(c_+)$ is in S_{split} whereas $X(c_-)$ is a representative of \overline{X}_{l} in C_{split} .

To show that the operation \tilde{M}_{m_L} is equal to M_{m_L} , as defined in equations (1) and (2), one can analyze how \tilde{M}_{m_L} acts on the computational basis, i.e. we track how it transforms the stabilizers of those states. For example, the state $|00\rangle$ is stabilized by Z_1 and Z_2 , this means that

$$\begin{split} \tilde{M}_{+}|00\rangle &= (E_{\mathrm{merged}})^{\dagger} \cdot E_{\mathrm{split}}|00\rangle \\ &= (E_{\mathrm{merged}})^{\dagger} \cdot E_{\mathrm{split}} \cdot Z_{1}|00\rangle \\ &= (E_{\mathrm{merged}})^{\dagger} \cdot \overline{Z}_{1} \cdot E_{\mathrm{split}}|00\rangle \\ &= Z \cdot (E_{\mathrm{merged}})^{\dagger} \cdot E_{\mathrm{split}}|00\rangle \\ &= Z \cdot \tilde{M}_{+}|00\rangle, \end{split}$$

and therefore $\tilde{M}_{+}|00\rangle$ is stabilized by Z. Here, we have used the properties of the encoding isometries and the fact that a representative \overline{Z}_1 for $C_{\rm split}$ is also a representative \overline{Z} for $C_{\rm merged}$. Doing the same with the other stabilizer, Z_2 , also yields Z as a stabilizer (so Z_1Z_2 yields the identity). One can also verify that $\tilde{M}_{+}|00\rangle$ is not stabilized by -Z by reversing the previous equalities and therefore $\langle Z \rangle$ is the full stabilizer group of $\tilde{M}_{+}|00\rangle$. Looking now at $\tilde{M}_{-}|00\rangle$ one can see that Z_2 also yields Z but Z_1 will yield -Z, indeed

$$\begin{split} \tilde{M}_{-}|00\rangle &= (E_{\mathrm{merged}})^{\dagger} \cdot E_{\mathrm{split}} \cdot X_{\mathrm{l}}|00\rangle \\ &= (E_{\mathrm{merged}})^{\dagger} \cdot E_{\mathrm{split}} \cdot X_{\mathrm{l}} \cdot Z_{\mathrm{l}}|00\rangle \\ &= -(E_{\mathrm{merged}})^{\dagger} \cdot \overline{Z}_{\mathrm{l}} \cdot E_{\mathrm{split}} \cdot X_{\mathrm{l}}|00\rangle \\ &= -Z \cdot (E_{\mathrm{merged}})^{\dagger} \cdot E_{\mathrm{split}} \cdot X_{\mathrm{l}}|00\rangle \\ &= -Z \cdot \tilde{M}_{-}|00\rangle. \end{split}$$

Hence, $\tilde{M}_{-}|00\rangle$ is both stabilized by Z and -Z, and is therefore the null vector. In other words, the state $|00\rangle$ will never give an outcome -1 for m_L , which is what we expect.

The full results (shown in table A1) indicate that

$$\begin{split} \tilde{M}_{+} &= \alpha_{+} |0\rangle \langle 00| + \beta_{+} |1\rangle \langle 11| \\ \tilde{M}_{-} &= \alpha_{-} |0\rangle \langle 01| + \beta_{-} |1\rangle \langle 10|, \end{split}$$

for some non-zero complex numbers α_{\pm} and β_{\pm} . To complete the proof, we verify that there are no relative phases or amplitude differences between α_{\pm} and β_{\pm} . To see that, one can look at the action of \tilde{M}_{m_L} on the Bell states. For \tilde{M}_+ we look at the Bell state ($|00\rangle + |11\rangle$)/ $\sqrt{2}$, stabilized by $\langle X_1X_2, Z_1Z_2\rangle$ and for \tilde{M}_- the Bell state ($|01\rangle + |10\rangle$)/ $\sqrt{2}$ stabilized by $\langle X_1X_2, -Z_1Z_2\rangle$. The important fact is that a representative $\overline{X_1}\overline{X_2}$ for $C_{\rm split}$ is also a representative of \overline{X} for $C_{\rm merged}$. That is to say

$$\begin{split} \tilde{M}_{+} \frac{|00\rangle + |11\rangle}{\sqrt{2}} &= \gamma_{+} \frac{|0\rangle + |1\rangle}{\sqrt{2}} \\ \tilde{M}_{-} \frac{|01\rangle + |10\rangle}{\sqrt{2}} &= \gamma_{-} \frac{|0\rangle + |1\rangle}{\sqrt{2}}, \end{split}$$

for some non-zero complex numbers γ_{\pm} . By linearity of \tilde{M}_{m_L} we can conclude that $\alpha_+ = \beta_+ = \gamma_+$ and that $\alpha_- = \beta_- = \gamma_-$. In conclusion, we have shown that $\tilde{M}_{m_L} \propto M_{m_L}$, meaning that it performs the desired logical operation.

Table A1. How \tilde{M}_{\pm} transforms the computational basis states characterized by their stabilizer group.

		$ ilde{M}_+$		$ ilde{M}_{-}$	
$\mathcal S$	State	S	State	S	State
$\langle Z_1, Z_2 \rangle$	00⟩	$\langle Z \rangle$	0⟩	$\langle Z, -Z \rangle$	0
$\langle Z_1, -Z_2 \rangle$	$ 01\rangle$	$\langle Z, -Z \rangle$	0	$\langle Z \rangle$	$ 0\rangle$
$\langle -Z_1, Z_2 angle$	$ 10\rangle$	$\langle -Z, Z \rangle$	0	$\langle -Z \rangle$	$ 1\rangle$
$\langle -Z_1, -Z_2 \rangle$	$ 11\rangle$	$\langle -Z \rangle$	$ 1\rangle$	$\langle -Z, Z \rangle$	0

A.2. Split operation

For the Z-split operation one reverses the roles of $C_{\rm split}$ and $C_{\rm merged}$. The starting point is the same as shown in figure 3(b), without \pm terms in the middle. Then, in order to split the patch, one has to split each four-body X stabilizer in the middle row into a pair of two-body X stabilizers. Those stabilizers are shown with \pm signs on figure 3(c). They commute with everything except for the central row of Z-plaquettes. One can see that measuring them will remove those Z-plaquettes from the stabilizer group, but keep the product of all those plaquettes, the logical $\overline{Z_1} \overline{Z_2}$ of the two separate patches. Note that it is sufficient to measure only the top (or bottom) row of two-body X-checks as the bottom (or top) one is then the product of those and the previous four-body X-checks. This also means that the outcomes of those two-body checks are perfectly correlated between facing pairs. Letting $\mathcal I$ be the set of the top row of those checks and $m_p = \pm 1$ the measurement outcome of the two-body plaquette p, the operation performed is then

$$\prod_{p\in\mathcal{I}}\frac{\mathbb{1}+(-1)^{m_p}X(p)}{2}.$$

Then, to correct to standard surface codes with no remaining minus signs, one has to apply some of the previous Z-plaquettes that were removed from the stabilizer, correcting the correlated facing X-checks. Labeling the set of qubits affected by the correction c, one has

$$Z(c) \cdot \prod_{p \in \mathcal{I}} \frac{1 + (-1)^{m_p} X(p)}{2} = \prod_{p \in \mathcal{I}} \frac{1 + X(p)}{2} \cdot Z(c).$$

This operation corresponds to S_+ , defined in equation (1). If one wants to implement S_- , defined in equation (2), then one has to additionally apply a logical representative of X on the first patch, \overline{X}_1 . The choice of one or the other version is conditioned by the previous m_L outcome that we received during the merging step. Then, to show that this performs the correct logical operation, we analyze

$$\widetilde{S}_{m_L} = (E_{ ext{split}})^{\dagger} \cdot \overline{X}_1^{\frac{1-m_L}{2}} \cdot \prod_{p \in \mathcal{I}} \frac{1 + X(p)}{2} \cdot Z(c) \cdot E_{ ext{merged}},$$

which, using the properties of the encoding isometries, \tilde{S}_{m_l} simplifies to

$$\tilde{S}_{m_L} = X_1^{\frac{1-m_L}{2}} \cdot (E_{\text{split}})^{\dagger} \cdot E_{\text{merged}}.$$
(A5)

At this point, recalling equation (A4), we can see that

$$\tilde{S}_{+} = (\tilde{M}_{+})^{\dagger} = (M_{+})^{\dagger} = S_{+},$$

which concludes the proof of correctness for the split operation. Note that it was crucial to apply the intermediary Z-plaquettes (in \mathcal{L}_g) as the correction. If we had instead applied a string of Z-flips between the faulty X-plaquettes, the correction would not be absorbed in the encoding map of C_{merged} and moreover would anti-commute with any representative \overline{X} of C_{merged} or $\overline{X}_1\overline{X}_2$ of C_{split} and therefore flip the phase between the $|0\rangle$ and $|1\rangle$ states.

Appendix B. Example: code conversion as gauge fixing

To see the utility of gauge fixing for analyzing code conversion protocols, we consider two protocols for converting from the [7, 1, 3] Steane code to the [15, 7, 3] Reed–Muller code with six gauge Z operators fixed (see figure B1 for the stabilizers and gauge operators that define these codes). The first, from Anderson $et\ al\ [31]$, is based on the realization that the state $|\psi\rangle_{\text{Steane}} \otimes \frac{1}{\sqrt{2}}(|0\rangle_{\text{Steane}}|0\rangle + |1\rangle_{\text{Steane}}|1\rangle)$ is a code state of the Reed–Muller code with its horizontal X gauge logical operators fixed, see top-right of figure B2. Conversion from the Steane code to the Reed–Muller code then involves fault-tolerantly preparing the eight-qubit ancilla state and fixing the three appropriate Z gauge operators. The state is always stabilized by the Reed–Muller stabilizers, whose eigenvalues can be reconstructed from the checks which are measured at every round, preserving the code distance and allowing error correction by syndrome decoding.

The second scheme, from Colladay and Mueller [32], is not based on gauge fixing, and begins with the eight qubits needed for conversion initialized in the state $|0\rangle^{\otimes 8}$. This ensures that the initial checks anticommute with

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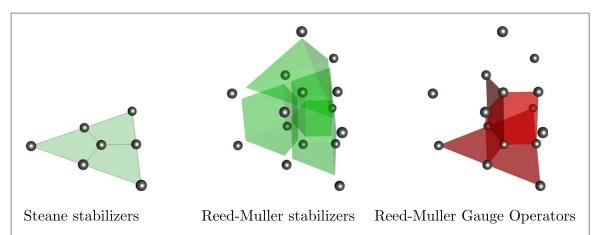


Figure B1. Stabilizers of the Steane and Reed–Muller codes, and Z gauge operators of the Reed–Muller code. Red tinting on a face or volume indicates the presence of a Z operator on the vertices which make up that face or volume. For example, there are six Reed–Muller gauge operators of the form $Z^{\otimes 4}$, supported on the red-tinted quadrilaterals seen on the right. Green tinting indicates the presence of both an X and a Z stabilizer operator.

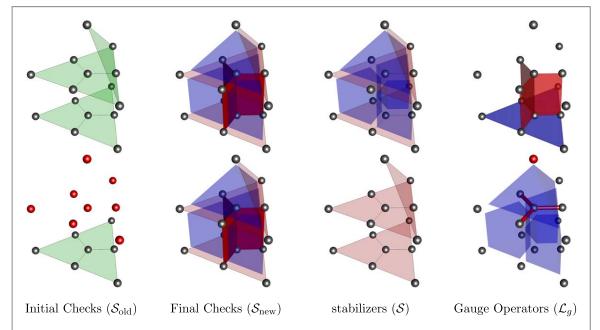


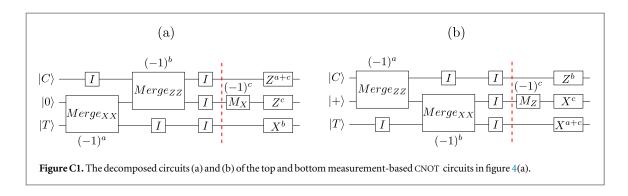
Figure B2. Comparison between Steane-to-Reed–Muller conversion schemes from [31] (top) and [32] (bottom). Red and green tinting match figure B1, blue tinting indicates an X operator supported on the vertices of the tinted face or volume. Tinted vertices/edges indicate weight-one/two operators supported on the tinted vertex/edge. In the Anderson scheme, the subsystem code which applies during the code deformation is made explicit; it is the distance-three Reed–Muller code. The Colladay scheme, however, does not have any X operators in the relevant stabilizer, S, so the distance of the relevant subsystem code is only 1, see section 4. Note: Gauge operators in the top right should also be present in the bottom right, they are not drawn here for clarity.

any potential *X* stabilizer supported on the final eight qubits, so that the only operators whose eigenvalues can be reconstructed from the measured operators are *Z* operators, preventing the correction of *Z* errors (see figure B2 for a graphical comparison of these code conversion protocols). The difference in fault tolerance between these two protocols which accomplish the same task provides us with a good motive to incorporate subsystem codes into the analysis of code deformation and lattice surgery, considered in the main text.

Examining the Criterion 1 from section 4.1, one can see that the Anderson scheme has an underlying subsystem code with distance 3, whereas not having any *X*-stabilizers, the Colladay scheme has an underlying subsystem code with distance 1.

Appendix C. Disparity in error rates of CNOT gates

A joint measurement is realized by performing a merge and a split operation in sequence. In our simulation, the circuits in figure 4(a) are decomposed into the ones in figure C1. Figure C2 shows that the rates of $\overline{X}/\overline{Z}$ errors on the control and target qubits are different for the rotated surface code with d=5. This disparity can be



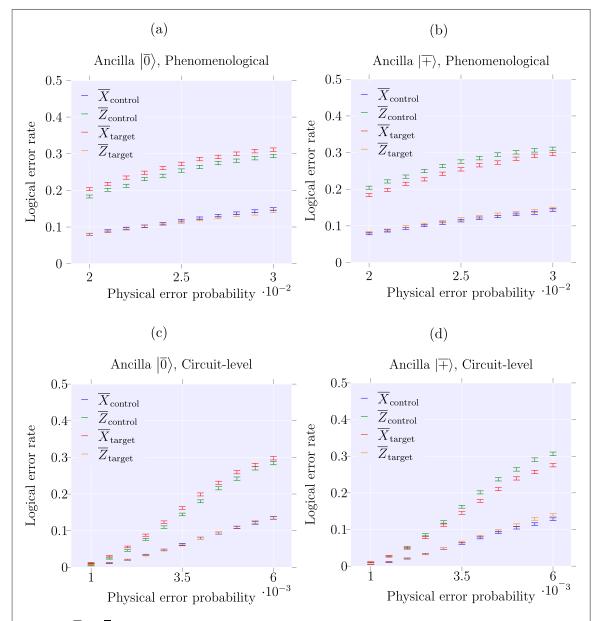


Figure C2. \overline{X} and \overline{Z} error rates on the control and target qubits for lattice-surgery-based CNOT operations at distance 5. (a) and (b) Correspond to the phenomenological error model, (c) and (d) correspond to the circuit-based error model. The disparity in error rates is explained by error propagation through the measurement-based circuit implementing the CNOT.

explained using a toy model to account for propagation of logical errors through measurement-controlled corrections. In this toy model, identity gates result in an \overline{X} or \overline{Z} error with probability p (\overline{Y} errors are assumed to occur with probability $\sim p^2$, since the minimum-weight \overline{Y} operator has weight 2d-1 in the surface code). The merge operations are modeled as ideal joint measurements, followed by an error of the form $\overline{X} \otimes \overline{\mathbb{I}}$, $\overline{\mathbb{I}} \otimes \overline{X}$, $\overline{Z} \otimes \overline{\mathbb{I}}$, or $\overline{\mathbb{I}} \otimes \overline{Z}$, each occurring with probability p, since these are the likeliest logical errors. If a logical Pauli error occurs, it propagates forward through the circuit, changing the measured eigenvalue for any measurement

operator with which it anticommutes. For example, if an $\overline{X} \otimes \overline{1}$ error occurs after the M_{XX} operation in figure 4(a) (in which the ancilla begins in the $|0\rangle$ state), the measured value b will be mapped to 1-b, causing an \overline{X} operator to be incorrectly applied to the target qubit at the end of the $\overline{\text{CNOT}}$. It is easy to confirm that there are 7 such first-order errors which result in an \overline{X} error on the target qubit, 6 errors which result in a \overline{Z} error on the control qubit, and 3 errors which result in the other logical errors shown in figures C2(a) and (c) (a similar analysis holds for the error rates shown in figures C2(b) and (d)). The biased logical error rates predicted by this simplified model are in good agreement with the logical error rates observed in simulation, shown in figure C2. Preventing this bias from growing during the execution of a long algorithm, by appropriate selection of decomposition for CNOTs, is likely an important step in the design of high-performance fault-tolerant circuits for quantum computation.

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