

Contents

Table of contents	xv
List of figures	xix
List of tables	xxiii
1 Introduction	1
1.1 Fabrication technologies	2
1.2 Generic integration technologies	3
1.3 Generic design libraries	4
1.4 Objectives	7
1.5 Thesis outline	7
2 Arrayed waveguide grating (AWG)	9
2.1 Background and motivation	9
2.2 AWG operation description	10
2.2.1 Field at the output waveguides	13
2.2.2 Arbitrary IW position	13
2.3 Indium Phosphide AWGs	13
2.3.1 Overview	13
2.3.2 Design, fabrication and characterization	14
2.4 Silicon on Insulator AWGs	22
2.4.1 Overview	22
2.4.2 Design and simulation	23
2.4.3 Device fabrication and characterization	25
2.4.4 Phase error measurements	27
2.5 Conclusions	29
3 Interleave-chirped arrayed waveguide grating (IC-AWG)	33
3.1 Background and motivation	33
3.2 Model and design procedure	34
3.2.1 AWG layout	34

3.2.2	Field at the arrayed waveguides	35
3.2.3	Second Free Propagation Region	37
3.2.4	Field at the output waveguides	38
3.2.5	Arbitrary IW position	38
3.2.6	Modeling polarization dependence	39
3.2.7	Design procedure	40
3.3	Experimental validation	42
3.3.1	Design	43
3.3.2	Characterization	44
3.4	Conclusions	50
4	Reflective arrayed waveguide grating (R-AWG)	53
4.1	Background and motivation	53
4.2	Operation principle	54
4.2.1	The Sagnac Loop Reflector (SLR)	55
4.2.2	Formulation	56
4.2.3	Gaussian spectral response	57
4.3	Design, fabrication and characterization	59
4.3.1	Design and simulation	60
4.3.2	Device fabrication and characterization	61
4.4	Flattened and arbitrary spectral responses	63
4.4.1	Flattened response	63
4.4.2	Arbitrary spectral responses	65
4.5	Conclusions	70
5	Wavelength scanning AWGs using Surface Acoustic Waves	73
5.1	Introduction	73
5.2	Operation principle	74
5.3	AlGaAs technology	77
5.4	Design, fabrication and characterization	77
5.5	Conclusion	80
6	Thesis conclusions and outlook	81
6.1	Conclusions	81
6.2	Outlook	83
Appendix A	Silicon Nitride AWGs	87
A.1	Overview	87
A.2	Design, fabrication and characterization	88
Appendix B	AWG layouts	93
B.1	Orthogonal layout	93
B.2	Smit layout	95
B.3	S-type layout	95
Appendix C	AWG libraries	97

C.1	Introduction	97
C.1.1	Libraries provided in this version	97
C.1.2	Available waveguides from each foundry	97
C.1.3	Different layouts	98
C.1.4	Single-etched and double-etched AWG	98
C.1.5	AWG layout with parameters	98
C.2	Drawing an AWG	98
C.2.1	Loading the AWG module	99
C.2.2	Creating the AWG	101
C.2.3	Specifying data export options	101
C.2.4	Specifying the technology and waveguide parameters	102
C.2.5	Specifying design parameters	102
C.2.6	Calling the constructor, designing and drawing	103
C.3	Simulating the AWG in Matlab	104
C.3.1	Simulator files	104
C.3.2	Optodesigner AWG output files	104
C.3.3	AWG simulator main file	105
C.3.4	Simulation output	106
Appendix D AWG channel cross-talk definition		109
Appendix E Optical frequency domain reflectometry (OFDR)		111
Appendix F List of publications		113
F.1	SCI Journal papers	113
F.2	Conference papers	114
F.3	Papers in other journals	115
F.4	Patents	115
F.5	Awards	116
References		117